DS2400SPE Series

2400 Watts Distributed Power System

Total Power: 2400 Watts

at high line

Input Voltage: 90-140 Vac

180-264 Vac

of Outputs: Main and Standby

Special Features

- · 2400W output power at high line
- High power and short form factor
- · 1U power supply
- High density design: 62 W/in³
- · Active power factor correction
- · Inrush current control
- · 80 Plus Platinum efficiency
- · N+1 or N+N redundant
- · Active current sharing
- PMBusTM compliant
- Two-year warranty
- · Class A Conducted / Radiated EMI
- RoHS

Safety

UL/cUL 60950 (UL Recognized) DEMKO+CB Report EN60950

EN60950

CE Mark

BIS

BSMI KC

EAC



Product Descriptions

The DS2400SPE series power supply features an input range of 90-140 Vac, and 180-264 Vac. It employs active power factor correction to minimize input harmonic current distortion and to ensure compliance with the international EN61000-3-2 standard - they have a power factor of 0.99 at full load. The power supplies also feature active AC inrush control, to automatically limit inrush current at turn-on to 45 A maximum.

The power supply employs an ultra high efficiency conversion topology, together with an innovative power transformer and rectifier construction that further improves power density and reduces interconnect power losses. Users have a choice of standard I²C or advanced PMBusTM communications. The control software runs under Windows on any standard PC, and uses a highly intuitive graphical user interface to simplify power supply set-up.

The DS2400SPE series can deliver up to 196.72 A (at high line) from its main 12 Vdc output, and up to 3.5 A from its 12 Vdc standby output. The form factor is 1U and can be used in single or in redundant configurations.

DS2400SPE series complies with 80plus Platinum Efficiency, its efficiency achieves 94% at 230Vac with 50% full load.



Model Numbers

Standard	Output Voltage	Minimum Load¹	Maximum Load	Stand-By Supply	Air Flow Direction
DS2400SPE-3	12.2Vdc	0A	196.72A	12Vdc@3.5A	Normal (DC Connector to Handle)
DS2400SPE-3-001	12.2Vdc	0A	196.72A	12Vdc@3.5A	Reverse (Handle to DC Connector)

Note 1 – 1A minimum current needed for transient load response testing. Unit is designed to operate and be within output regulation range at zero load.

Options

None

Electrical Specifications

Absolute Maximum Ratings

Stress in excess of those listed in the "Absolute Maximum Ratings" may cause permanent damage to the power supply. These are stress ratings only and functional operation of the unit is not implied at these or any other conditions above those given in the operational sections of this TRN. Exposure to any absolute maximum rated condition for extended periods may adversely affect the power supply's reliability.

Table 1. Absolute Maximum Ratings:

Parameter	Model	Symbol	Min	Тур	Max	Unit
Input Voltage AC continuous operation	All models	V _{IN,AC}	90 180	- -	140 264	Vac Vac
Maximum Output Power V _{IN,AC} = 90 - 140 Vac V _{IN,AC} =180 - 264 Vac	All models All models	P _{O,max}	- -	-	1400 2400	W W
Isolation Voltage Input to outputs Input to safety ground	All models			-	2951 4243	Vdc Vdc
Ambient Operating Temperature ¹	Forward air Reverse air	T _A	0 0	-	+50 +40	°C °C
Storage Temperature	All models	T _{STG}	-40	-	+70	оС
Humidity (non-condensing) Operating Non-operating	All models All models		5 5		95 95	% %
Altitude ² Operating Non-operating	All models All models		-	-	10,000 50,000	feet feet
MTBF Telcordia Issue 3	All models		200,000	-	-	Hours
Operating Life	All models		5	-	-	Years

Note 1 - Forward air: allowable up to 60 °C at 1800W high line / 1200W low line. Reverse air: allowable up to 50 °C at 1700 W high line / 1200W low line.

Note 2 - Derating please see page 20.

Input Specifications

Table 2. Input Specifications:

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Operating Input Voltage, AC Low Line High Line	All	V _{IN,AC}	90 180	115 230	140 264	Vac Vac
Operating Input Voltage, DC	All	$V_{\rm IN,DC}$	180	264	300	Vdc
Input AC Frequency		f _{IN,AC}	47	50/60	63	Hz
Input AC Start-up Voltage Low Line High Line		V _{IN,AC-start}	84 174	- -	90 180	Vac Vac
Input AC Undervoltage Lockout Voltage Low Line High Line		V _{IN,AC-stop}	- -	- -	80 170	Vac Vac
Input DC Undervoltage Lockout Voltage		V _{IN,DC-stop}	-	-	170	Vdc
Maximum Input Current $(I_O = I_{O,max}, I_{SB} = I_{SB,max})$	$V_{IN,AC} = 100Vac$ $V_{IN,AC} = 180Vac$	I _{IN,max}	-	-	18.5 16.0	А
No Load Input Power $(V_O = On, I_O = 0A, I_{SB} = 0A)$	All	P _{IN,no-load}	-	-	6	W
Harmonic Line Currents	All	THD	F	er EN / IE	C 61000-3	-2
ITHD	$V_{IN,AC} = 230 Vac$ $I_O = 50-100 \% I_{O,max}$		-	-	5	%
Power Factor	I _O > 20%I _{O,max}	PF	0.9	-	-	
Startup Surge Current (Inrush)	V _{IN,AC} = 264Vac	I _{IN,surge}	-	-	45	Apk
Input Fuse	Internal, L 5x20mm, Fast Acting 20A, 420Vac/Vdc		-	-	20	А
Leakage Current to earth ground	$V_{IN,AC} = 254Vac$ $f_{IN,AC} = 60Hz$ $UL1950$ measurement method		-	-	0.57	mA
Hold-up Time	$I_{O} = 20\%I_{O,max}$		10	-	-	mSec
Operating Efficiency @ 25°C	$V_{IN,AC} = 230 Vac$ $I_{O} = 10 \% I_{O,max}$ $I_{O} = 20 \% I_{O,max}$ $I_{O} = 50 \% I_{O,max}$ $I_{O} = 100 \% I_{O,max}$	η	89 93 94 91.5	- - -	- - -	% % %

Output Specifications

Table 3. Output Specifications:

Parameter		Condition	Symbol	Min	Тур	Max	Unit
Factory Cat Valtage		V _{IN.AC} = 230Vac	Vo	12.175	12.20	12.225	Vdc
Factory Set Voltage		$I_{\rm O} = 50\%I_{\rm O,max}$	V _{SB}	11.95	12.00	12.05	vac
Output Regulation		Inclusive of set-point, temperature change,	V _o	11.60	12.20	12.90	Vdc
Output Negulation		warm-up drift and dynamic load	V_{SB}	11.40	12.00	12.60	Vuc
Output Ripple, pk-pk		Measure with a 0.1µF ceramic capacitor in parallel with a 10µF	V _o	-	-	180	mV _{PK-PK}
σαιραί (πρρίο, μπ μπ		tantalum capacitor, 0 to 20MHz bandwidth	V_{SB}	-	-	120	··· • PK-PK
Output Current ¹		$V_{IN,AC} = 90-140Vac$ $V_{IN,AC} = 180-264Vac$	I _O	-	1 1	114.75 196.72	A
		All	I _{SB}	0.1	-	3.5	
Number of Parallel Units		Main Output "I_SHARE" connected		-	-	4	
V _O Current Share Accuracy ²		10-100% I _O I _O < 10%I _{O,max}		-	-	8 10	А
Load Canacitanas		Turn-on / Turn-off	Vo	-	-	38000	uF
Load Capacitance	Load Capacitance		V _{SB}	-	-	4700	uF
V _O Dynamic Response ³ Pea	k Deviation	1A min. with 18A step, or 8A min. with 40A step, or 10A min. with 50% I_O step; slew rate = 0.5A/ μ s; 4000 μ F output cap.	Vo	11.60	-	12.90	V
V _O Long Term Stability Max change over 24 hours		After thermal equilibrium (30 mins) $V_{IN,AC} = 100/200 Vac$	Vo	-	-	0.5	±%V _O
	ase Margin Sain Margin			- -	45 -	- -6	Ø dB

Note 1 – 1A minimum current for dynamic response testing. Unit is designed to operate and be within output regulation range at zero load.

Note 2 - The current sharing function will start when the total system load has reached 7% of the power supply rating.

Note 3 - 1A minimum current for dynamic response, dynamic load frequency is from 50Hz to 10kHz.

System Timing Specifications

Table 4. System Timing Specifications:

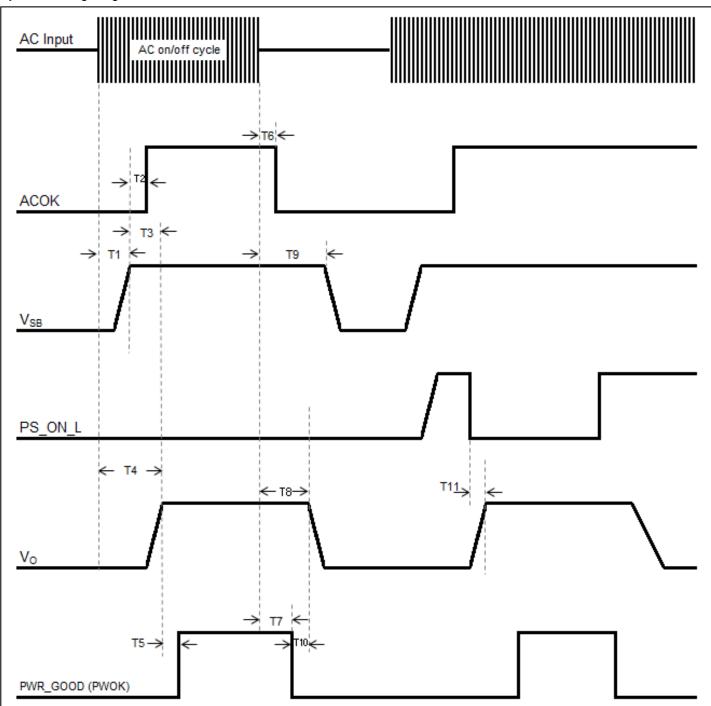
Label	Parameter	Min	Тур	Max	Unit
T1	Delay from AC being applied to V _{SB} being within regulation	20	-	2000	mSec
T2 ¹	Delay from standby output to ACOK assertion	-	-	20	mSec
ТЗ	Delay from standby output to main output voltage being within regulation	-	-	350	mSec
T4	Delay from AC being applied to main output being within regulation	-	-	2300	mSec
T5	Delay from output voltages within regulation limits to PWR_GOOD/PWOK assertion	100	-	500	mSec
Т6	Delay from loss of AC to deassertion of ACOK	ı	-	7	mSec
T7	Delay from loss of AC to deassertion of PWR_GOOD/PWOK	10	-	-	mSec
T8	Delay from loss of AC to main output being within regulation	11	-	-	mSec
T9 ²	Delay from loss of AC to standby output being within regulation	150	-	-	mSec
T10	Delay from deassertion of PWOK to output falling out of regulation	1	-	-	mSec
T11	Delay from PS_ON_L assertion to output being within regulation	-	-	350	mSec

Note 1 - ACOK can assert earlier than the standby output

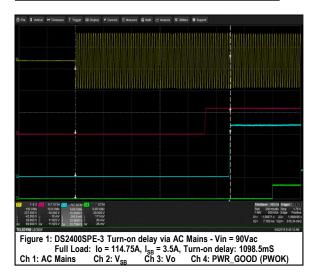
Note 2 - Measured with standby output loaded at 1A, no load at main output.

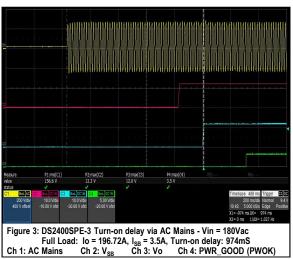
System Timing Specifications

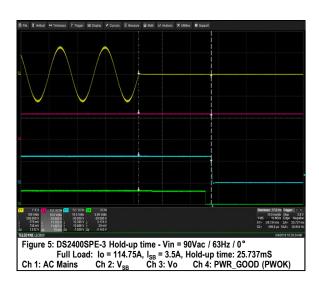
System Timing Diagram:

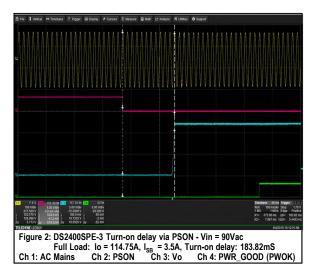


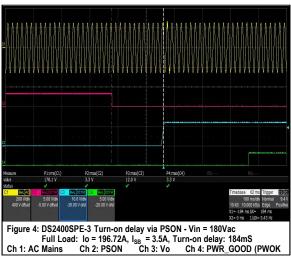
DS2400SPE-3 Performance Curves

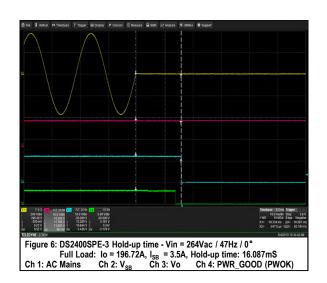




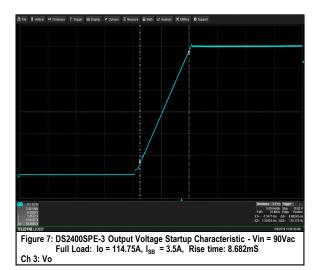


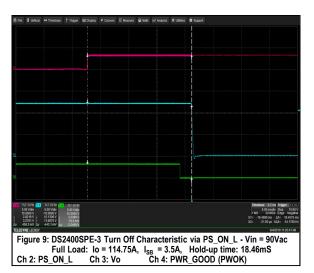


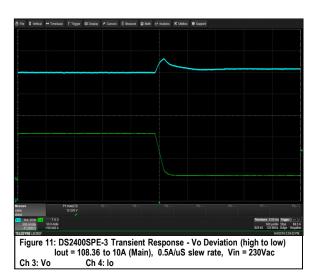


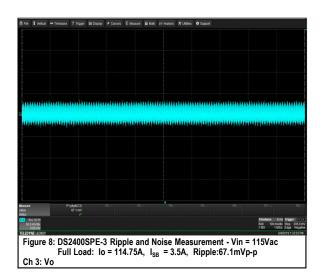


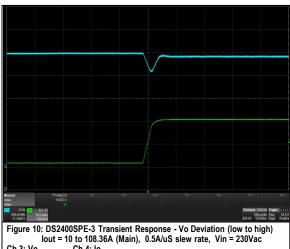
DS2400SPE-3 Performance Curves



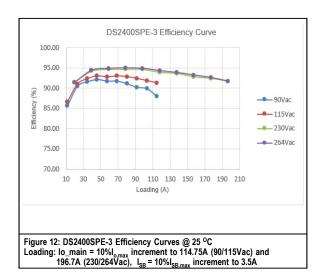








Ch 3: Vo Ch 4: lo



Protection Function Specification

Input Fusing

DS2400SPE series is equipped with an internal non user serviceable 20A @ 420Vac/Vdc fuse for fault protection on L line input.

Over Voltage / Under Voltage Protection (OVP / UVP)

The main and standby output is protected against over-voltage according to the limits set in below table. When the main output / standby OVP circuit is activated, the power supply will latch off, require PS_ON_L or the input power to be recycled manually to reset the power supply after the fault has been removed.

The power supply main output will shut down if it drops to undervoltage limits below.

OVP

Parameter	Min	Nom	Max	Unit	Protection Mode
V _O Output Overvoltage	13.5	/	14.5	V	Latch
V _{SB} Output Overvoltage	13.5	/	15	V	Auto-retry

UVP

Parameter	Min	Nom	Max	Unit	Protection Mode
Vo Output Undervoltage	/	/	9.6	V	Latch
V _{SB} Output Undervoltage	10.0	/	10.5	V	Auto-retry

Over Temperature Protection (OTP)

The power supply is internally protected against over temperature conditions. When the OTP limit is reached, all outputs, except standby, will shut-down and will remain off until the over-temperature condition no longer exists.

There is hysteresis point between the OTP threshold and the recovery point to ensure there is no frequent on-off cycling of the outputs. Upon reaching the temperature recovery point, all outputs will auto-recover.

Any OTP fault will be reported in the PMBus status flag.

Over Current Protection (OCP)

The DS2400SPE main output is internally protected against output overload or short circuit applied to its output. If the over-current is not more than 120% and does not last for more than 55ms, the power supply continues to operate. Latch occurs when the over-current exceeds the conditions mentioned. Any over-current above 170% (+/-10% tolerance) causes the power supply to latch immediately within 10ms. The latched state requires PS_ON_L or the input power to be recycled to reset the power supply after the fault has been removed. A fault in the main output does not cause the standby output to shut down.

The Standby has an OCP limit from 110% to 150% and auto-retry when the overload is removed. A fault in the Standby Output shuts down other outputs and auto-recovers when the overload on the Standby is removed.

Parameter	Min	Nom	Max	Protection Mode
V _O Output Overcurrent	120%	/	170%	Latch if the overload >55mS
Vo Output Overcurrent	170%	/	/	Latch immediately
V _{SB} Output Overcurrent	110%	/	150%	Shutdown and auto-retry

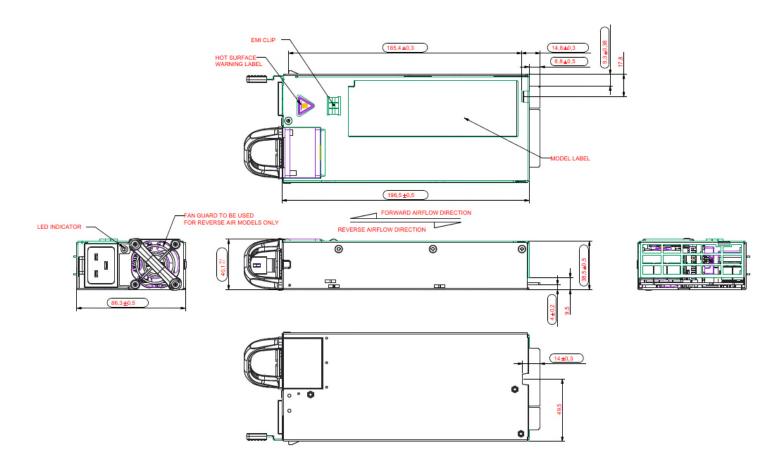
Short Circuit Protection (SCP)

The DS2400SPE series power supply protects against a short circuit, which is defined as an impedance of 0.06 ohm or less, applied to any output during start-up or while running. When the main output is shorted, the power supply latches off immediately. The latched state requires AC power / PS_ON_L recycling to restart the power supply.

When the Standby Output is shorted, the output goes into "hiccup mode". When the Standby Output attempts to restart, the maximum peak current from the Standby Output is less than 10A. The maximum average current, taking into account the "hiccup" duty cycle, does not exceed the rated output current of the Standby.

Mechanical Specifications

<u>Detailed Mechanical</u> (Unit: mm)



The DS2400SPE series weight is 1160g/2.56lbs.

Connector Definitions

AC Input Connector

Pin 1 - L Pin 2 - N

Pin 3 - Earth Ground

Output Connector - Power Blades

Output Connector - Control Signals

S1 - PS_PRESENT S2 - RESERVED S3 - RESERVED

S4 - PWR_GOOD (PWOK)
S5 - ACOK (AC Input Present)

S6 - RETURN
S7 - I_SHARE
S8 - RESERVED

S9 - PS_INTERRUPT_L/ALERT

 S10
 RETURN

 S11
 RESERVED

 S12
 RESERVED

 S13
 PS_ON_L

 S14
 PSKILL_H

 S15
 RESERVED

 S16
 RETURN

S17 - SDA

S18 - RETURN

S19 - SCL

S20 - RETURN

S21 - REMOTE SENSE-

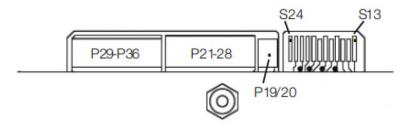
S22 - RETURN

S23 - REMOTE SENSE+

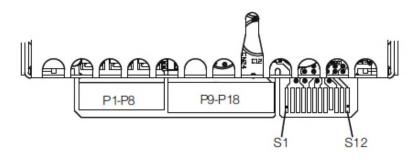
S24 - RESERVED



Power Supply Output Card Edge (Bottom Side)



Power Supply Output Card Edge (Top Side)



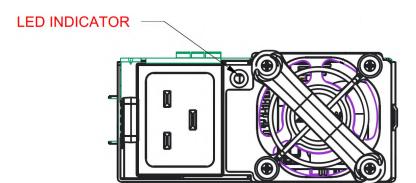
Power / Signal Mating Connectors and Pin Types

Table 5. Mating Connectors for DS2400SPE series:

Reference	On Power Supply	Mating Connector or Equivalent
AC Input Connector	IEC320-C20	IEC320-C19
Output Connector	Card-edge	FCI 10107844-002LF or any equivalent

LED indicator Definition

One bi-color (green/amber) LED at the power supply input side provides status signal. The status LED conditions are shown on the below table



Conditions	LED Status
No input to PSU	Off
Main output ON	Solid Green
Standby mode (PS_ON_L = High)	Blinking Green
Power supply failure (OCP,OVP,OTP, etc.)	Blinking Amber

Environmental Specifications

EMC Immunity

DS2400SPE series power supply is designed to meet the following EMC immunity specifications

Table 6. Environmental Specifications:

Standard	Description
EN 55032/FCC/CFR47	Radiated Emissions, 30M -1GHz, Class A
EN 55032/FCC/CFR47	Conducted Emissions, 150k-30MHz, Class A
IEC/EN 61000-3-2	Harmonics - AC supply <16 Amps per phase
IEC/EN 61000-3-3	Voltage Fluctuations - AC supply <16 Amps per phase
IEC/EN 61000-4-2	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Electrostatic discharge immunity test. +/-15KV air, +/-8KV contact discharge, performance Criteria B
IEC/EN 61000-4-3	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Radiated, radio-frequency, electromagnetic field immunity test 10 V/m performance Criteria A
IEC/EN 61000-4-4	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Electrical Fast Transient/Burst Immunity Test. +/- 2KV for AC power port, performance Criteria B; +/- 0.5KV for AC power port, performance Criteria A.
IEC/EN 61000-4-5	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Surge Test. 2KV common mode and 1KV differential mode for AC ports, performance criteria A.
IEC/EN 61000-4-6	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Conducted, radio-frequency, electromagnetic field immunity test 10 V/m performance Criteria A.
IEC/EN 61000-4-11	Electromagnetic Compatibility (EMC) - Testing and measurement techniques: Voltage Dips and Interruptions: Criteria B: >95% reduction for 10mS Criteria B: >30% reduction for 500mS Criteria C: or >95% reduction for 500mS

Notes1: Performance Criteria as defined by EN 300 386

Performance Criteria A: The apparatus will continue to operate as intended after the test. No degradation of performance or loss of function is allowed below specified performance level during intended use of operation.

Performance Criteria B: The apparatus will continue to operate as intended after the test. No degradation of performance or loss of function is allowed below specified performance level during intended use of operation. Degradation of performance is allowed during the exposure to an electromagnetic phenomenon but no change of actual operating state is allowed.

Performance Criteria C: Temporary loss of function is allowed, provided the function is self-recoverable or can be restored by the operation of the controls.

Safety Certifications

The DS2400SPE series power supply is intended for inclusion in other equipment and the installer must ensure that it is in compliance with all the requirements of the end application. This product is only for inclusion by professional installers within other equipment and must not be operated as a stand alone product.

Table 7. Safety Certifications for DS2400SPE series power supply system:

Document	Description
UL/cUL 60950 (UL Recognized)	US Requirements
DEMKO+CB Report EN60950	(All CENELEC Countries)
CE Mark	European Requirements
CQC	Chinese Requirements
KC	Korea Requirements
EAC	Russia Requirements
BSMI	Taiwan Requirements
BIS	India Requirements

EMI Emissions

The DS2400SPE series meet the Class A limits of EMI requirements of FCC 47CFR15 Subpart B and the limits specified in CISPR22/EN55032.

Conducted Emissions

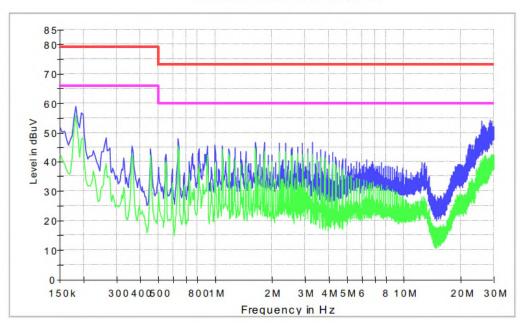
The applicable standard for conducted emissions is FCC 47CFR15 Subpart B and CISPR22/EN55032. Conducted noise can appear as both differential mode and common mode noise currents. Differential mode noise is measured between the two input lines, with the major components occurring at the supply fundamental switching frequency and its harmonics. Common mode noise, a contributor to both radiated emissions and input conducted emissions, is measured between the input lines and system ground and can be broadband in nature.

The DS2400SPE series power supplies have internal EMI filters to ensure the convertors' conducted EMI levels comply with CISPR22/EN55032 Class A limits. The EMI measurements are performed with resistive loads at maximum rated loading.

Below is sample of Conducted EMI Measurement at 230Vac/50Hz input.

Line:

Conducted Emission at AC power



Note: Red Line refers to Quasi Peak limit, Pink Line refers to the Quasi Average limit.

Conducted EMI emission specifications of the DS2400SPE series

Parameter	Model	Symbol	Min	Тур	Max	Unit
FCC 47CFR15 Subpart B	All	Margin	6	-	-	dB
CISPR 22 (EN55032) class A	All	Margin	6	-	-	dB

Technical Reference Note

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Radiated Emissions

Unlike conducted EMI, radiated EMI performance in a system environment may differ drastically from that in a stand-alone power supply. The shielding effect provided by the system enclosure may bring the EMI level from Class A to Class B. It is thus recommended that radiated EMI be evaluated in a system environment. The applicable standard is EN55032 Class A (FCC Part 15). Testing AC-DC converters as a stand-alone component to the exact requirements of EN55032 can be difficult, because the standard calls for 1m leads to be attached to the input and outputs and aligned such as to maximize the disturbance. In such a set-up, it is possible to form a perfect dipole antenna that very few AC-DC converters could pass. However, the standard also states that an attempt should be made to maximize the disturbance consistent with the typical application by varying the configuration of the test sample.

Operating Temperature

The DS2400SPE series power supplies operating temperature requirements (air inlet temperature) please refer to below.

Medal	Conditions	Output Operating Temp		ıg Temp.	Altitudo	System Back
Model	Conditions	Power	Min	Max	Altitude	Pressure (in H2O)
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1400W 2400W	0 °C	50 °C	950m/3,100ft	0.0
DS2400SPE-3	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1400W 2200W	0 °C	50 °C	3050m/10,000ft	0.0
D324003PE-3	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1200W 1800W	0 °C	60 °C	3050m/10,00 ft	0.0
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1350W 2000W	0 °C	50 °C	3050m/10,000ft	0.5
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1400W 2200W	0 °C	40 °C	950m / 3100ft	0.0
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1400W 2400W	0 °C	40 °C	Sea Level	0.0
DS2400SPE-3 -001	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1400W 1900W	0 °C	40 °C	3050m/10,000ft	0.0
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1200W 1700W	0 °C	50 °C	3050m/10,000ft	0.0
	$V_{IN,AC} = 90-140 Vac$ $V_{IN,AC} = 180-264 Vac$	1350W 1800W	0 °C	40 °C	3050m/10,000ft	0.5

Storage and Shipping Temperature

The DS2400SPE series power supply can be stored or shipped at temperatures between -40 °C to +70 °C.

Altitude

The DS2400SPE series will operate within specifications at altitudes up to 10,000 feet above sea level. The power supply will not be damaged when stored at altitudes of up to 50,000 feet above sea level.

Humidity

The DS2400SPE series will operate within specifications when subjected to a relative humidity from 5% to 95% non-condensing. The DS2400SPE series can be stored in a relative humidity from 5% to 95% non-condensing.

Vibration

The DS2400SPE series power supply pass the following vibration specifications:

Non-Operating Random Vibration

Acceleration	3.13		gRMS	
Frequency Range	5-500	Hz		
Duration	30	30		
Direction	6 mutually perpendicular axis			
PSD Profile	FREQ 5 Hz 50 Hz 500 Hz	SLOPE <u>dB/oct</u> 	PSD <u>g²/Hz</u> 0.000595 g²/Hz 0.03 g²/Hz 0.0585 g²/Hz	

Operating Random Vibration

Acceleration	0.63		gRMS	
Frequency Range	5-500	Hz		
Duration	10	mins		
Direction	3 mutually perpendicular axis			
PSD Profile	FREQ 5 Hz 50 Hz 500 Hz	SLOPE <u>dB/oct</u> 	PSD <u>g²/Hz</u> 0.000882 g²/Hz 0.000882 g²/Hz 0.0004332 g²/Hz	

Shock

The DS2400SPE series power supply will pass the following shock specifications:

Non-Operating Half-Sine Shock

Acceleration	40	G	
Duration	15	msec	
Pulse	Half-Sine		
No. of Shock	3 shock on each of 6 faces		

Operating Half-Sine Shock

Acceleration	30	G	
Duration	11	msec	
Pulse	Half-Sine		
No. of Shock	3 shock on each of 6 faces		

Power and Control Signal Descriptions

AC Input Connector

This connector supplies the AC Mains to the DS2400SPE series power supply.

Pin 1 - L

Pin 2 - N

Pin 3 - Earth Ground

Output Connector - Power Blades

These pins provide the main output for the DS2400SPE series. The Main Output (V_0) and the Output Return pins are the positive and negative rails, respectively, of the V_0 main output of the DS2400SPE series power supply. The Main Output (V_0) is electrically isolated from the power supply chassis.

P1-P8 - Main Output (V_O) P9-P18 - Output Return

P19-P20 - Standby Output (Vsb)

P21-P28 - Output Return P29-P36 - Main Output (V_O)

Output Connector - Control Signals

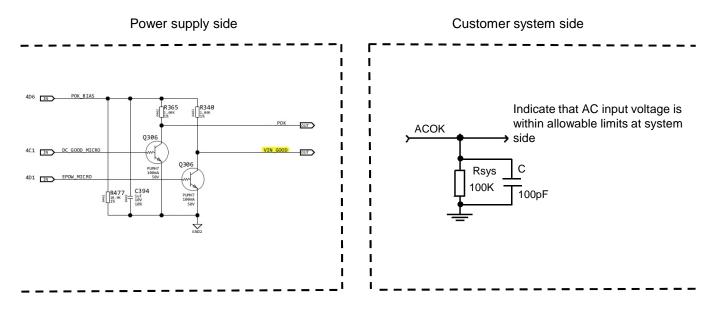
The DS2400SPE series contains a 24 pins control signal header providing an analogue control interface and I²C interface signal connections.

ACOK (AC Input Present) - (Pin S5)

The ACOK is an open collector signal which is normally HIGH (>2.0V) whenever input AC voltage is within allowable limits. This signal will go LOW (<0.4V) within 6ms from loss of AC. Power supply has internal 1 Kohm pull-up resistor to internal bias. Additional pull-up on system side may be added but current-limited to 0.7mA. Suitable noise filter capacitor connected to standby return line is recommended on system side.

ACOK Signal Electrical Characteristics:

High = OK, Low = Not OK.					
Parameter	Min	Max	Unit		
Output High Voltage	2.4	3.6	V		
Output Low Voltage	0.0	0.4	V		
Output Signal Source Current	-	2	mA		
Output Signal Sink Current	-	4	mA		
Output Rise and Fall Time (Zero decoupling capacitor)	-	100	uSec		

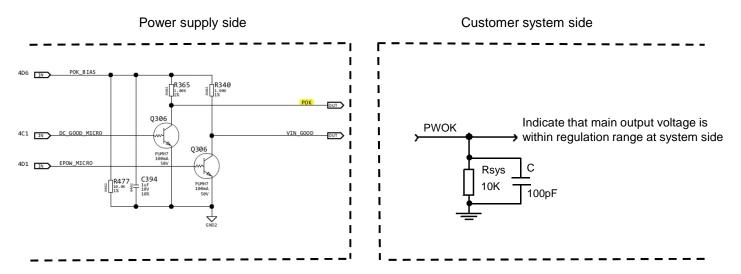


PWR_GOOD (PWOK) - (Pin S4)

A power good signal will be asserted, driven HIGH (>2,0V), by the power supply to indicate that all outputs are valid. If the main output falls below 10.9V for any reason, then this output will be driven LOW (<0.4V). This signal has 1K pull-up resistor connected to Standby bus before Oring device inside PSU.

PWOK Signal Electrical Characteristics:

High = OK, Low = Not OK.					
Parameter	Min	Max	Unit		
Output High Voltage	2.4	3.6	V		
Output Low Voltage	0.0	0.4	V		
Output Signal Source Current	-	2	mA		
Output Signal Sink Current	-	4	mA		
Output Rise and Fall Time (Zero decoupling capacitor)	-	100	uSec		

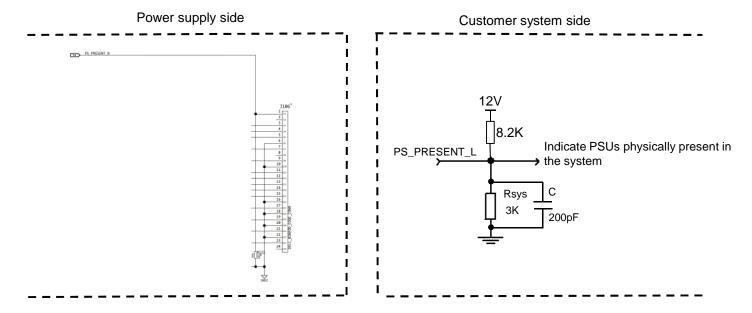


PS_PRESENT_L- (Pin S9)

This signal pin is grounded inside the power supply. It can be used to sense PSUs seated in the system by using a suitable pull-up to Standby bus with a noise filter capacitor connected to Standby Output return.

PS_PRESENT_L Signal Electrical Characteristics:

High = PSU not present, Low = PSU present				
Parameter Min Max Unit				
Signal Sink Current when LOW	-	4	mA	
Signal Sink Current when HIGH - 50 uA				



I_SHARE (Current Share Bus) - (Pin S7)

This signal is a bus which will allow two or more power supplies to share the system load current.

This signal will have a voltage which is directly proportional to supplied current, and be represented by 7*lout/Imax. A linear slope from minimum load to full load is expected. The I_SHARE voltage will be within the voltage range specified in below table. It is capable of sinking 0.4mA and sourcing 4mA.

I_SHARE Signal Accuracy:

Lood (nor nowar ounnly unit)	I_SHARE Signal Voltage (Vdc)			
Load (per power supply unit)	Min.	Тур.	Max.	
100%	6.912	7.0	7.088	
50%	3.412	3.5	3.588	
0%	0.000	0.0	0.450	

The I_SHARE signal can be disabled by shorting this pin to ground. The main output voltage will stay within regulation limits in this condition.

PS_ON_L (Remote On/Off) - (Pins S13)

This signal is active low signal, enables or disables the main output of the power supply. It has 10K internal pull-up resistor, no additional pull-up required by system.

When the signal is pulled low (<0.8V) by the system, the main output will be enabled. The signal can source a maximum of 1mA in this state. Pulling this signal to high (>2.0V) will shutdown the main output. This signal can be pulled high to 5.0V maximum. The standby output is not affected by this signal. This signal is defined by the logic table below.

PS ON L Signal Logic Table:

PS_ON_L	PSKILL_H	Main Output State
Low	Low	On
Low	Open	Off
Open	Low	Off
Open	Open	Off

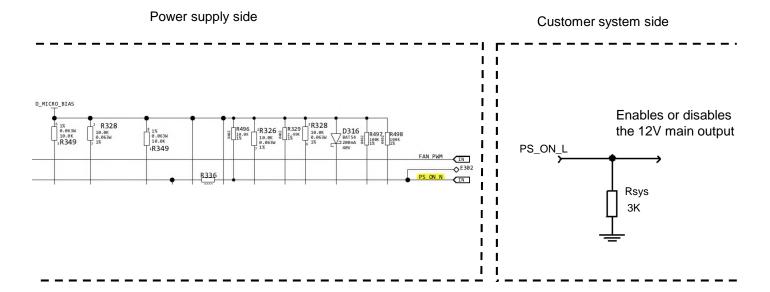
PS_ON_L Signal Electrical Characteristics:

High = PSU Off, Low = PSU On					
Parameter	Min.	Max.	Unit		
Input High Voltage	2.0	3.6	V		
Input Low Voltage	0.0	0.8	V		
Source Current when LOW	-	4	mA		
Expected Rise and Fall Time (zero decoupling capacitor)	-	500	uSec		

Artesyn Embedded Technologies

This function is supported through PMbus, please refer to section "DS2400SPE series Support PMBusTM Command List" for more details.

For proper power supply operation, it is recommended to provide separate PS_ON_L signal to each unit using suitable circuit capable to sink 4mA max. current when connected in parallel configuration.



PSKILL_H - (Pin S14)

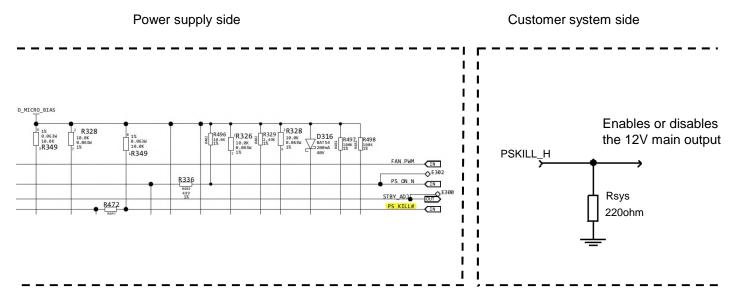
This signal has the shortest pin in the output connector. It functions as the first break/ last mate pin, thus, supports how-swap capability. This enables or disables the 12.2V main output of the power supply. When this signal is opened by the power supply removal from the system, the main output will immediately shut down.

PSKILL H Signal Electrical Characteristics

High = PSU Off, Low = PSU On						
Parameter	Min.	Max.	Unit			
Input High Voltage	2.0	3.6	V			
Input Low Voltage	0.0	0.8	V			
Source Current when LOW	-	4	mA			
Expected Rise and Fall Time (zero decoupling capacitor)	-	500	uSec			

Technical Reference Note

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REMOTE SENSE +, REMOTE SENSE - (Pins S21, S23)

The power supply main output is equipped with remote sense on the REMOTE SENSE + and REMOTE SENSE - pins. This remote sense circuit can compensate for a power path drop of 200mV on each sense line. It will not raise the power supply's output voltage to the OVP trip level.

SDA, SCL, and PS_INTERRUPT_L

Please refer to "Communication Bus Descriptions" section.

Communication Bus Descriptions

I²C Bus Signals

DS2400SPE series power supply contains enhanced monitor and control functions implemented via the I²C bus. The DS2400SPE series I²C functionality (PMBusTM and FRU data) can be accessed via the output connector control signals. The communication bus is powered either by the internal 3.3V supply or from an external power source connected to the Standby Output (i.e. accessing an unpowered power supply as long as the Standby Output of another power supply connected in parallel is on).

If units are connected in parallel or in redundant mode, the Standby Outputs must be connected together in the system. Otherwise, the I²C bus will not work properly when a unit is inserted into the system without the DC source connected.

Note: $PMBus^{TM}$ functionality can be accessed only when the PSU is powered-up. Guaranteed communication I^2C speed is 100K Hz.

Power Supply Addressing

The DS2400SPE standard power supply have the fixed address: B0h.

SDA, SCL (I²C Data and Clock Signals) - (Pins S17, S19)

SDA and SCL are bi-directional serial bus lines for communication for PMBus devices in the power supply and the host system. These pins are internally pulled up to internal bias supply with a 100K resistor. These pins are recommended to be pulled-up in the system by an 2.2K ohm resistor to 3.3V and a 200pF decoupling capacitor at the system side.

If units are connected in parallel or redundant mode, the stand-by output must be capable of maintaining supply to the power supply controller such that I2C communication is not lost even without an AC supply in one power supply.

If these pins are pulled up to the Stand-by output created from the Main output using a step-down, non-isolated DC/DC provided within the end system, the ground of the Stand-by output and Main output must be connected together.

PS_INTERRUPT_L- (pin S9)

PS_INTERRUPT_L is used to send a signal to the system that a fault in the power supply occurred. This signal is normally logic level HIGH. It will go to a LOW logic level when a fault bit has been set in the power supply's status register. To reset the PS_INTERRUPT_L signal back to normal (logic HIGH level), perform one of the following actions - (1) recycle input AC power, (2) toggle PSON signal and (3) issuance of a CLEAR_FAULTS PMBusTM command.

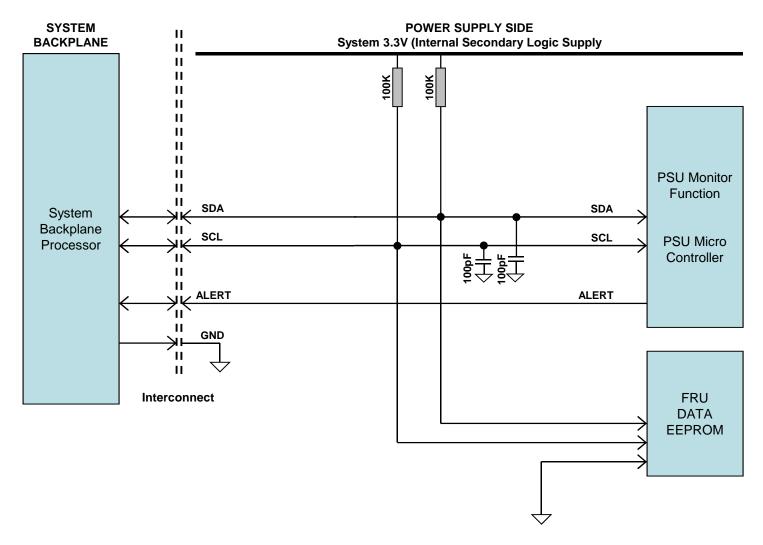
I²C Bus Communication Interval

The interval between two consecutive I²C communications to the power supply should be at least 15ms to ensure proper monitoring functionality.

I²C Bus Signal Integrity

The noise on the I²C bus (SDA, SCL lines) due to the power supply will be less than 300mV peak-to-peak. This noise measurement should be made with an oscilloscope bandwidth limited to 100MHz. Measurements should be make at the power supply output connector with 10K ohm resistors pulled up to Standby Output and 47pF ceramic capacitors to Standby Output Return.

I²C Bus Internal Implementation, Pull-ups and Bus Capacitances



I²C Bus - Recommended external pull-ups:

Electrical and Interface specifications of I^2C signals (referenced to Standby Output Return pin, unless otherwise indicated):

Parameter	Condition	Symbol	Min	Тур	Max	Unit
SDA, SCL internal pull-up resistor		R _{int}	-	100	-	Kohm
SDA, SCL recommended external bus capacitance		C _{ext}	-	100	-	pF
Recommended external pull-up resistor	1 to 4 PSU	R _{ext}	-	2.2	-	Kohm

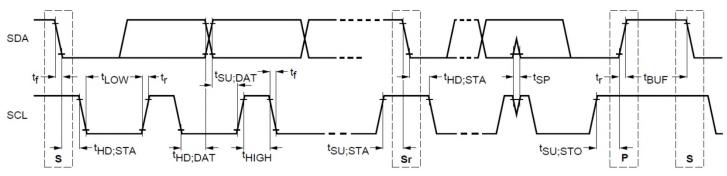
Logic Levels

DS2400SPE series power supply I²C Communication Bus will respond to logic levels as per below:

Logic High: 3.3V Nominal (Specs is 2.1V to 5.5V)** Logic Low: 500mV nominal (Specs is 2000mV max)**

**Note: Artesyn 73-769-001 I²C adapter was used.

Timings



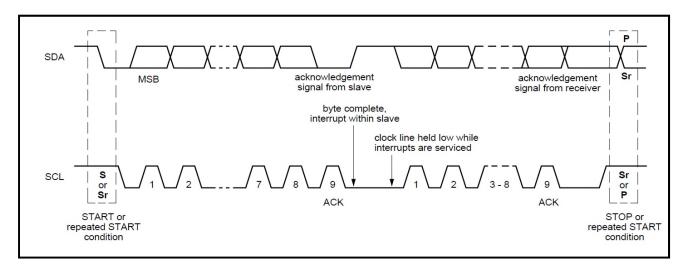
Danamatan	Completed	Standard-l	Mode Specs	A advisal Bu	11	
Parameter	Symbol	Min	Max	Actual Measured		Unit
SCL Clock Frequency	f _{SCL}	0	100	99	.74	KHz
Hold time (repeated) START condition	t _{HD;STA}	4.0	-	4.73		μS
LOW period of SCL clock	t _{LOW}	4.7	-	4.	91	μS
HIGH period of SCL clock	t _{HIGH}	4.0	50	4.	16	μS
Setup time for repeated START condition	t _{SU;STA}	4.7	-	4.87		μS
Data hold time	t _{HD;DAT}	0	3.45	1.7		μS
Data setup time	t _{SU;DAT}	250	-	5029		nS
Rise time	t _r	-	1000	SCL = 916 SDA = 914.4		nS
Fall time	t _f	-	300	SCL = 136.7 SDA = 145.1		nS
Setup time for STOP condition	t _{SU;STO}	4.0	-	5.37		μS
Bus free time between a STOP and START condition	t _{BUF}	4.7	-	63.5***		mS

^{***} Note Artesyn 73-769-001 I²C adapter (USB-to-I²C) and Universal PMBus™ GUI software was used

I²C Clock Synchronization

The DS2400SPE series power supply apply clock stretching. An addressed slave power supply hold the clock line (SCL) low after receiving (or sending) a byte, indicating that it is not yet ready to process more data. The system master that is communicating with the power supply will attempt to raise the clock to transfer the next bit, but must verify that the clock line was actually raised. If the power supply is clock stretching, the clock line will still be low (because the connections are open-drain).

The maximum time out condition for clock stretching for DS2400SPE series is 30 mS.



FRU (EEPROM) Data

The FRU (Field Replaceable Unit) data format is compliant with the Intel IPMI v1.0 specification.

The DS2400SPE-3 uses 1 page of EEPROM for FRU purpose. A page of EEPROM contains up to 256 byte-sized data locations.

Where: OFFSET

-The OFFSET denotes the address in decimal format of a particular data byte within

DS2400SPE-3 EEPROM.

VALUE

-The VALUE details data written to a particular memory location of the EEPROM.

DEFINITION - The contents DEFINITION refers to the definition of a particular data byte.

OFF	SET	DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
	-	COMMON HEADER, 8 BYTES	•	
0	00	FORMAT VERSION NUMBER (Common Header)	1	01
		7:4 - Reserved, write as 0000b		
		3:0 - Format Version Number = 1h for this specification		
1	01	INTERNAL USE AREA OFFSET	22	16
2	02	CHASSIS INFO AREA OFFSET	1	1
3	03	BOARD INFO AREA OFFSET	0	00
4	04	PRODUCT INFO AREA OFFSET	4	04
5	05	MULTI RECORD AREA OFFSET	13	0D
6	06	PAD (reserved) Default value is 0.	0	00
7	07	ZERO CHECK SUM (256 – (Sum of bytes 0 to 6))	215	D7
		CHASSIS INFO AREA(24 BYTES)		
8	08	FORMAT VERSION NUMBER	1	01
		7:4 - Reserved, write as 0000b		
		3:0 - Format Version Number = 1h for this specification		
9	09	CHASSIS INFO AREA LENGTH in multiple of 8 bytes	3	03
10	0A	CHASSIS TYPE (Default value is 0.)	0	00
		CHASSIS PART NUMBER Type/Length CAh (if used)		
11	0B	Type = "ASCII+LATIN1" = (11)b Length = 10 Bytes = (001010)b	202	CA
12 13	0C 0D	CHASSIS PART NUMBER BYTES (Default value is 0.)	0	00 00
14	0E		0	00
15	0F		0	00
16	10		0	00
17	11		0	00
18	12		0	00
19	13		0	00
20 21	14 15		0	00 00
22	16	CHASSIS SERIAL NUMBER Type/Length CFH (if used)	199	C7
22	10	Type = "ASCII+LATIN1" = (11)b Length = 15 Bytes = (001111)b	199	
23	17	CHASSIS SERIAL NUMBER BYTES, Default value is 0.	0	00
24	18		0	00
25	19		0	00
26	1A		0	00
27	1B		0	00
28 29	1C 1D		0	00 00
30	1E	End Tag (Default value is 0.)	193	C1
	1F		+	
31	1F	Zero Check Sum (From 8d to 30d if used)	170	AA

OFF	SET	DEFINITION	SPEC	VALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		PRODUCT INFORMATION AREA, 72 BYTES	•	
32	20	FORMAT VERSION NUMBER (Product Info Area)	1	01
		7:4 - Reserved, write as 0000b		
		3:0 - Format Version Number = 1h for this specification		
33	21	PRODUCT INFO AREA LENGTH (In multiples of 8 bytes)	9	9
34	22	Language	25	19
35	23	Manufacturer Name Type/Length , 7-Byte Allocation = C7H	199	C7
		MANUFACTURER'S NAME 7 bytes sequence		
36	24	"A"= 41h	65	41
37	25	"R"= 52h	82	52
38	26	"T"= 54h	84	54
39	27	"E"= 45h	69	45
40	28	"S"= 53h	83	53
41	29	"Y"=59H	89	59
42	2A	"N"=4EH	78	4E
43	2B	Product Name Type/Length, 15-Byte Allocation = CFH	207	CF
		Product Name, 12 Byte sequence		
44	2C	"D" = 44H	68	44
45	2D	"S" = 53H	83	53
46	2E	"2" = 32H	50	32
47	2F	"4" = 34H	52	34
48	30	"0" = 30H	48	30
49	31	"0" = 30H	48	30
50	32	"S" = 53H	83	53
51	33	"P" = 50H	80	50
52	34	"E" = 45H	69	45
53	35	"-" = 2DH	45	2D
54	36	"3" = 33H	51	33
55	37	3 – 3311	32	20
56	38		32	20
50 57	39		32	20
58	3A		32	20
59	3B	Part/Model Number Type/Length, 15-Byte Allocation = CFH	207	CF
		Part / Model Number		<u> </u>
60	3C	"D" = 44H	68	44
61	3D	"S" = 53H	83	53
62	3E	"2" = 32H	50	32
63	3F	"4" = 34H	52	34
64	40	"0" = 30H	48	30
65	41	"0" = 30H	48	30
66	42	"S" = 53H	83	53
67	42	5 = 53H "P" = 50H	80	50
68	43 44	"E" = 45H		45
		"-" = 2DH	69	
69 70	45 46	- = ZDH "3" = 33H	45 51	2D
70 71	46 47	S = 5511	51	33
71	47		32	20
72	48		32	20
73	49		32	20
74	4A	PRODUCT VERGION NUMBER To the disc Decision of the Control of the	32	20
75	4B	PRODUCT VERSION NUMBER Type/Length 2-Byte Allocation = C2H (Per Unit)	194	C2
76	4C	"0" = 30H	48	30
77	4D	"A" = 41H	65	41
78	4E	PRODUCT SERIAL NUMBER Type/Length, 13-Byte Allocation = CDH	205	CD

OFF	SET	DEFINITION	SPEC '	VALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Model ID		
79	4F		XX	XX
80	50		XX	XX
81	51		XX	XX
82	52		XX	XX
		MANUFACTURING YEAR AND WEEK CODE		
83	53		XX	XX
84	54		XX	XX
0.5		Unique Serial Number		\ \v\
85 86	55 56		XX	XX
87	56 57		XX	XX
88	58		XX	XX
		MODEL REVISION	701	701
89	59	SHOULD TRACK MODEL Revision indicated on Model Label	XX	XX
90	5A	OHOOLD TITAOR WODEL REVISION INdicated on Wodel Laber	XX	XX
		MANUFACTURING LOCATION	7.01	7.0.
91	5B	"P" In Decimal = 080 In Hex = 50H	80	50
92	5C	Product Serial Number: ASSET TAG (Default = 0)	0	0
93	5D	End Tag In Decimal: 193 In Hex: 0C1H	193	C1
0.4		Reserved		00
94	5E		0	00
95 96	5F 60		0	00
97	61		0	00
98	62		o o	00
99	63		0	00
100	64		0	00
101	65		0	00
102	66		0	00
103	67	ZERO CHECK SUM (Per Unit)	XX	XX
		Multi Record Area, 72 Bytes		
		Power Supply Record Header (72 Bytes)		
104	68	Record Type ID (0x00 = Power Supply Information)	0	0
105	69	3-0: (0010)b, Record Format Version	2	2
106	6A	Record Length: 24 Bytes	24	18
107	6B	Record Checksum (Zero Checksum From 109d To 132d)	196	C4
108	6C	Header Checksum (Zero Checksum From 104d To 107d)	34	22
		Power Supply Record		1
1.5-		Overall Capacity of the Power Supply, 2 Bytes Sequence, 1200W = 0960H		
109	6D	15-12: (0000)b, Reserved	96	60
110	6E	11-0: 2400W = 0960H	09	09
	65	Peak VA,2 Bytes Sequence	055	
111	6F	15-12: (0000)b, Reserved	255	FF FF
112	70	11-0: FFFFH if not specified	255	FF
440	 4	Inrush Current, 45A	4-	0.5
113	71	In Hex = 2DH	45	2D
	70	Inrush Interval,	200	
114	72	In Decimal = 200 In Hex = C8H	200	C8

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Low End Input Voltage Range 1(10mV), (90V / 10mV) 9000 = 2328H		
		2 Bytes Sequence		
115 116	73 74	In Decimal = 040, 035 In Hex = 28H, 23H	40 35	28 23
110	74	High End Input Voltage Range 1(10mV), (140V/10mV) 14000= 36B0H	33	23
		2 Bytes Sequence		
117	75	In Decimal = 176, 054	176	В0
118	76	In Hex = B0H, 36H	54	36
		Low End Input Voltage Range 2(10mV)		
440	77	180V = 18000 (x10mV) = 4650H	00	50
119 120	77 78	In Decimal = 080, 070 In Hex = 50H, 46H	80 70	50 46
120		High End Input Voltage Range 2(10mV)		
		264V = 26400 (x10mV) = 6720H		
121	79	In Decimal = 032, 103	32	20
122	7A	In Hex = 20H, 67H	103	67
123	7B	Low End Input Frequency Range, 47Hz = 2FH	47	2F
124	7C	High End Input Frequency Range, 63Hz = 3FH	63	3F
125	7D	AC Dropout Tolerance in ms, 10mS= 0AH	10	0A
126	7E	Binary Flags, 1 indicates function supported and a 0 indicates function not supported.	26	1A
		Bits 7-5: RESERVED, WRITE AS 000B Bit 4: Tachometer Pulses Per Rotation / Predictive Fail Polarity BIT = 0		
		Bit 3: Hot Swap / Redundancy Support BIT = 1		
		Bit 2: Auto switch Support BIT = 0		
		Bit 1: Power Factor Correction Support BIT = 1		
		Bit 0: Predictive Fail Support BIT = 0		
107	70	Peak Wattage Capacity and Holdup Time Not Applicable	0	00
127 128	7F 80	Bits 15-12: Holdup Time in Seconds Bits 11- 0: Peak Capacity in Watts	0 0	00 00
120		Combined Wattage, Not Applicable		- 00
129	81	Byte 1: Bits 7-4: Voltage1 Bits 3-0: Voltage2	0	00
130	82	Byte 2 and Byte 3: Total Combined Wattage	0	00
131	83	Stored with LSB first then MSB	0	00
132	84	Predictive Fail Tachometer Lower Threshold, Not Applicable.	0	00
		Predictive Failure is not Supported.		
		12V DC OUTPUT RECORD HEADER		
133	85	Record type = 01 for DC Output Record End of List /Record Format Version Number for 12V DC Output Record	1	01
134	86	Record Length of 12V DC Output Record: 13 Bytes	2	02
135	87	Record CHECKSUM of 12V DC Output Record (Zero CHECKSUM) (256-(sum of bytes 138 to 150)	13	0D
136	88	Header CHECKSUM of 12V DC Output Record Header (Zero CHECKSUM) (256-(sum of bytes	238	EE
137	89	133 to 136)	02	02
		12V OUTPUT RECORD		<u> </u>
138	8A	Output Information, 001 = 01H	1	01
		Nominal Voltage (10mV), (12V / 10mV) 1200 = 04B0H		
139	8B	2 Bytes Sequence In Decimal: 176, 004	176	В0
140	8C	In Hex: B0H, 04H	4	04
1.5		Maximum Negative Voltage Deviation (10mV), 1160 = 0488H	<u> </u>	
		2 Bytes Sequence		
141	8D	In Decimal: 136, 004	136	88
142	8E	In Hex: 88H, 04H	4	04

DS2400SPE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC	/ALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Maximum Positive Voltage Deviation (10mV), 1280 =0500H		
4.40	0.5	2 Bytes Sequence	00	00
143	8F		00	00
144	90		5	5
		Ripple and Noise pk-pk (mV), 120 = 78H		
1.45	0.1	2 Bytes Sequence	120	70
145 146	91 92	In Decimal: 120, 000 In Hex: 78H, 00H	120 0	78 00
140	92		0	00
		Minimum Current Draw (10mA), 0050 = 0032H		
1.17	02	2 Bytes Sequence In Decimal: 050, 000	50	22
147 148	93 94	In Hex: 32H, 00H	50 0	32 00
140	94	·	0	00
		Maximum Current Draw (10mA), 196.7A = 19670 (x10mA) = 4CD6H		
140	05	2 Bytes Sequence	24.4	D6
149	95 96	In Decimal: 214, 076 In Hex: D6H, 4CH	214 76	4C
150	96	·	76	40
		12VSB OUTPUT RECORD HEADER		
151	97	Record type = 01 for DC Output Record	1	01
152	98	End of List /Record Format Version Number for 12VSB Output Record	130	82
153	99	Record Length of 12VSB Output Record: 13Bytes	13	0D
154	9A	Record CHECKSUM of 12VSB Output Record (Zero CHECKSUM)	129	81
455	0.0	(256-(sum of bytes 156 to 168)		
155	9B	Header CHECKSUM of 12VSB Output Record Header (Zero CHECKSUM)	239	EF
		(256-(sum of bytes 151 to 154)		
		12VSB OUTPUT RECORD		
		Output Information, 002 = 02H		
		Bit 7: Standby Information = 1B		
		Bits 6-4: Reserved, Write as 000B		
156	9C	Bits 3-0: Output Number 2 = 010B	130	82
		Nominal Voltage (10mV), 12.00V = 1200 (x10mV) = 04B0H		
157	9D	2 Bytes Sequence	176	В0
158	9E		4	04
		Maximum Negative Voltage Deviation (10mV), 11.40V = 1140 (x10mV) = 0474H		
159	9F	2 Bytes Sequence	116	74
160	A0		04	04
	_	Maximum Positive Voltage Deviation (10mV), 12.6V = 1260 (x10mV) = 04ECH	-	
404		2 Bytes Sequence	000	50
161	A1 A2	2 5).00 30400.00	236	EC 04
162	AZ		4	04
400	4.0	Ripple and Noise pk-pk (mV), 120mV = 0078H	400	70
163	A3	2 Bytes Sequence	120	78
164	A4		0	00
4.5-		Minimum Current Draw (10mA), 0010 = 000AH		
165	A5	2 Bytes Sequence	10	0A
166	A6		0	00
		Maximum Current Draw (10mA), 0350 = 015EH		
167	A7	2 Bytes Sequence	94	5E
168	A8		1	01
	'	OEM RECORD HEADER		
161	A1	Record type = C0H for OEM Record	192	C0
162	A2	End of List /Record Format Version Number for 3.3Vsb output Record	130	82
102	714	Tand of Electrocold Contract version realised for 5.5 value duput record	130	02

DS2400SPE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC	VALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
169	A9	RESERVED	0	00
170	AA	RESERVED	0	00
171	AB	RESERVED	0	00
172	AC	RESERVED	0	00
173 174	AD AE	RESERVED RESERVED	0 0	00 00
174	AF	RESERVED	0	00
176	B0	PAD (reserved), Default value is 0.	1	01
177	B1	(1715 (1866) 1864), Balduli Valdo le G.	0	00
178	B2		0	00
179	В3		0	00
180	B4		0	00
181	B5		0	00
182	B6		0	00
183	B7 B8		0	00 00
184 185	B9		0 0	00
186	BA		0	00
187	BB		0	00
188	BC		0	00
189	BD		0	00
190	BE		0	00
191	BF		0	00
192	C0		0	00
193	C1		0	00
194	C2		0	00
195	C3 C4		0	00 00
196 197	C5		0 0	00
198	C6		0	00
199	C7		Ö	00
200	C8		0	00
201	C9		0	00
202	CA		0	00
203	СВ		0	00
204	CC		0	00
205	CD		0	00
206 207	CE CF		0 0	00 00
208	D0		0	00
209	D1		0	00
210	D2		0	00
211	D3		0	00
212	D4		0	00
213	D5		0	00
214	D6		0	00
215	D7		0	00
216	D8		0	00
217 218	D9 DA		0 0	00 00
218	DB		0	00
220	DC		0	00
221	DD		0	00
222	DE		0	00
223	DF		0	00
224	E0		0	00
225	E1		0	00
226	E2		0	00
227	E3		0	00
			0	00

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DS2400SPE-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC	VALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		INTERNAL USE AREA, 40 BYTES		
228	E4	RESERVED, Default value is 0.	0	00
229	E5		0	00
230	E6		0	00
231	E7		0	00
232	E8		0	00
233	E9		0	00
234	EA		0	00
235	EB		0	00
236	EC		0	00
237	ED		0	00
238	EE		0	00
239	EF		0	00
240	F0		0	00
241	F1		0	00
242	F2		0	00
243	F3		0	00
244	F4		0	00
245	F5		0	00
246	F6		0	00
247	F7		0	00
248	F8		0	00
249	F9		0	00
250	FA		0	00
251	FB		0	00
252	FC		0	00
253	FD		0	00
254	FE		0	00
255	FF		0	00

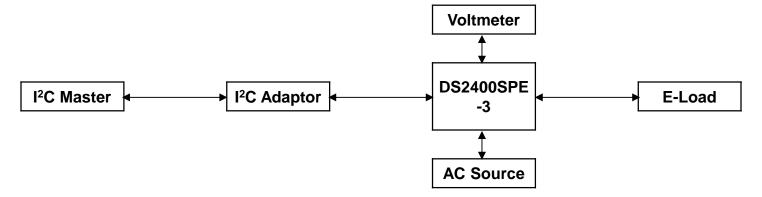
PMBus™ Interface Support

The DS2400SPE series is compliant with the industry standard PMBusTM protocol for monitoring and control of the power supply via the I^2C interface port.

DS2400SPE series PMBus™ General Instructions

Equipment Setup

The following is typical I²C communication setup:



DS2400SPE series Support PMBus™ Command List

The DS2400SPE is compliant with the industry standard PMBusTM protocol for monitoring and control of the power supply via the I^2C interface port.

DS2400SPE series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
00h	Page	00	R/W	1	Hex	
01h	OPERATION	80	R/W	1	Bitmapped	Used to turn the unit ON/OFF.
	b7:6					00 - Immediate Turn OFF (No Sequencing) 10 - PSU ON Margining not supported
	b5:4					Reserved
	b3:2					Reserved
	b1:0					Reserved
02h	ON_OFF_CONFIG	1D	R	1	Bitmapped	Configures the combination of CONTROL pin and serial communication commands needed to turn the Unit ON/OFF.
	b7:5					Reserved
	b4 – Enable CONTROL pin and Serial communication control.	1				0 – Unit powers up any time power is present regardless of the state of CONTROL pin. 1 – Unit powers up as dictated by CONTROL pin and OPERATION command (b3:0)
	b3 – Serial communication Control	1				0 – Unit Ignores ON/OFF portion of the OPERATION command.1 – Enables Serial communication ON/OFF portion of OPERATION command. Requires CONTROL pin to be asserted for the unit to start and energize the output.
	b2 – Sets how the unit responds to CONTROL pin	1				O – Unit ignores CONTROL pin. (ON/OFF controlled by OPERATION command). 1 – Unit requires CONTROL pin to be asserted to start the unit.
	b1 - CONTROL pin polarity	0				0 – Active Low (Pull Low to start the unit) 1 – Active high (Pull high to start the unit)
	b0 – CONTROL pin Action	0				0 – Use programmed turn ON/OFF delay 1 – Turn OFF the output and stop transferring energy to the output as fast as possible.
03h	CLEAR_FAULTS		S			
05h	PAGE_PLUS_WRITE	00	BW	Varies		
06h	PAGE_PLUS_READ	00	BR/BW	Varies		
10h	WRITE_PROTECT	80	R/W	1	Bitmapped	
	CAPABILITY	90	R	1	Bitmapped	Provides a way for the hosts system to determine some key capabilities of a PMBus [™] device.
405	b7 - Packet Error Checking					0 - PEC not supported 1 - PEC supported
19h	b6 - Maximum Bus Speed					0 - Maximum supported bus speed, 100KHz 1 - Maximum supported bus speed, 400KHz
	b5 - SMBALERT#					0 - SMBus Alert Pin not supported 1 - SMBus Alert Pin supported
	b4:0					Reserved

Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description	
1Ah	QUERY		BR/BW	2	Hex	Used to determine if the PSU supports a specific command; It should return the proper information about any commands listed	
1Bh	SMBALERT_MASK		BR/BW	2	Direct	Used with STATUS_INPUT, STATUS_TEMPERATURE, STATUS_IOUT	
20h	VOUT_MODE		R	2		Specifies the mode and parameters of Output Voltage related Data Formats	
21h	VOUT_COMMAND		R	2	Direct	Sets the Output Voltage Reference Vout command sends discreet value to change or trim output voltage. The value acts as Digital reference of the Power supply after additional operations are performed (to make the representation compatible). Affects OVP_WARNING and FAULT LIMIT, as well as POWER_GOOD_ON/OFF level.	
	COEFFICIENTS		BW/BR	5	Direct	Use to retrieve the m, b and R coefficients, needed for DIRECT data format	
30h	byte 5	00				R byte	
	byte 4:3	0000				b low Byte, b high byte	
	byte 2:1	0001				m low Byte, m high byte	
	FAN_CONFIG_1_2	90	R	1	Bitmapped		
	b7	1				0 - No fan is installed in position 1 1 - Fan is installed in position 1	
	b6	0				0 - Fan is commanded in RPM 1 - Fan is commanded is DC	
3Ah	b5:4	01				00 - 1 pulse per revolution 01 - 2 pulse per revolution 10 - 3 pulse per revolution 11 - 4 pulse per revolution	
	b3	0				1 – Fan is installed in position 20 – No Fan is installed in position 2	
	b2	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC	
	b1:0	00				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution	
3Bh	FAN_COMMAND_1	0000	R/W	2	Linear	Adjusts the operation of the Fans. The device may override the command, if it requires higher value, to maintain proper device temperature. Duty cycle Control - Commands Speeds from 0 to 100%	
40h	VOUT_OV_FAULT_LIMIT	801C	R/W	2	Linear	Sets Output Over voltage threshold. (14.25V)	
44h	VOUT_UV_FAULT_LIMIT	9812	R/W	2	Linear	Sets Under-voltage Fault threshold. (9.297V)	
46h	IOUT_OC_FAULT_LIMIT	D7F3	R/W	2	Linear	Sets the Over current threshold in Amps. (245.75A)	
4Ah	IOUT_OC_WARN_LIMIT	13F3	R/W	2	Linear	Sets the output Over Current Warning threshold in Amps. (196.75A)	

Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description
5Dh	IIN_OC_WARN_LIMIT	HL: 00DA LL: 50DA	R/W	2	Linear	Sets the input Over Current Warning threshold in Amps. (16A for High Line and 18.5A for Low Line)
6Bh	PIN_OP_WARN_LIMIT	HL: A312 LL: 9011	R/W	2	Linear	HL:2700W LL:1600W Sets the input Over Power Warning threshold in Watts. (2700W for High Line and 1600W for Low Line)
	STATUS_BYTE		R	1		Returns the summary of critical faults
	b6 – OFF					Unit is OFF
	b5 – VOUT_OV					Output over-voltage fault has occurred
78h	b4 – IOUT_OC					Output over-current fault has occurred
7011	b3 - VIN_UV					An input undervoltage fault has occurred
	b2 - TEMPERATURE					A temperature fault or warning has occurred
	b1 – CML					A communication, memory or logic fault has occurred.
	STATUS_WORD		R	2	Bitmapped	Summary of units Fault and warning status.
	b15 – VOUT					An output voltage fault or warning has occurred .
	b14 – IOUT					An Output current or power fault or warning has occurred.
	b13 – INPUT					An input voltage, current or power fault or warning as occurred.
	b11 - POWER_GOOD#					The POWER_GOOD signal is de-asserted
79h	b10 – FANS					A fan or airflow fault or warning has occurred.
	b9 - OTHERS					A bit in STATUS_OTHER is set.
	b6 – OFF					Unit is OFF
	b5 – VOUT_OV					Output over-voltage fault has occurred
	b4 - IOUT_OC					Output over-current fault has occurred
	b3 - VIN_UV					An input under-voltage fault has occurred
	b2 – TEMPERATURE					A temperature fault or warning has occurred
	b1 – CML					A communication, memory or logic fault has occurred.
	STATUS_VOUT		R	1	Bitmapped	
7Ah	b7 - VOUT Over-Voltage Fault					VOUT Overvoltage Fault
77311	b4 - VOUT Under-Voltage Fault					VOUT Under-voltage Fault
	STATUS_IOUT		R	1	Bitmapped	
7Bh	b7 - IOUT Overcurrent Fault					IOUT Overcurrent Fault
7511	b5 - IOUT Overcurrent Warning					IOUT Overcurrent Warning
	STATUS_INPUT		R	1	Bitmapped	Input related faults and warnings
	b7 - VIN_OV_FAULT					VIN Overvoltage Fault
7Ch	b4 - VIN_UV_FAULT					VIN Under voltage Fault
7011	b3 - Unit Off For Low Input Voltage					Unit is OFF for insufficient Input Voltage
	b1 - IIN_OC_WARNING					IIN Overcurrent Warning

Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description
	STATUS_TEMPERATURE		R	1	Bitmapped	Temperature related faults and warnings
7Dh	b7 - Over temperature Fault					Over temperature Fault
	b6 - Over temperature Warning					Over temperature Warning
	STATUS_CML		R	1	Bitmapped	Communications, Logic and Memory
7Eh	b7 - Invalid/Unsupported command					Invalid or unsupported Command Received
/ EII	b6 - Invalid/Unsupported Data					Invalid Data
	b5 - Packet Error Check Failed					Packet Error Check Failed
	STATUS_MFR_SPECIFIC		R	1	Bitmapped	
	b7 - Current Share Warning					Current Share Warning
	b6 - 12Vaux under-voltage or overcurrent fault					12Vaux under-voltage or overcurrent fault
	b5 - 12V Over-current Fault					12V Over-current Fault
80h	b4 - 12Vaux Over-voltage Fault					12Vaux Over-voltage Fault
	b3 - 12V Under-voltage Fault					12V Under-voltage Fault
	b2 - 12V Over-voltage Fault					12V Over-voltage Fault
	b1 - Thermal Fault					Thermal Fault
	b0 - Fan Fault					Fan Fault
	STATUS_FANS_1_2		R	1	Bitmapped	
81h	b7 - Fan1 Fault					Fan1 Fault
• • • • • • • • • • • • • • • • • • • •	b5 - Fan1 Warning					Fan1 Warning
	b3 - Fan 1 Speed Overridden					Fan 1 Speed Overridden
86h	READ_EIN		BR	6	Direct	Returns the accumulated input power over time
87h	READ_EOUT		BR	6	Direct	Returns the accumulated output power over time
88h	READ_VIN		R	2	Linear	Returns input Voltage in Volts ac.
89h	READ_IIN		R	2	Linear	Returns input Current in Amperes
8Bh	READ_VOUT		R	2	Linear	Returns the actual, measured voltage in Volts.
8Ch	READ_IOUT		R	2	Linear	Returns the output current in amperes.
8Dh	READ_TEMPERATURE_1 (Ambient)		R	2	Linear	Returns the ambient temperature in degree Celsius.
8Eh	READ_TEMPERATURE_2 (Hot Spot1)		R	2	Linear	Returns the hot pot 1 temperature in degree Celsius.
8Fh	READ_TEMPERATURE_3 (Hot Spot2)		R	2	Linear	Returns the hot pot 2 temperature in degree Celsius.
90h	READ_FAN_SPEED_1		R	2	Linear	Speed of Fan 1
96h	READ_POUT		R	2	Linear	Returns the output power, in Watts.
97h	READ_PIN		R	2	Linear	Returns the input power, in Watts.
98h	PMBus_REVISION		R	1	Bitmapped	Reads the PMBus revision number
99h	MFR_ID		BR, ASCII	4		Abbrev or symbol of manufacturers name.
9Ah	MFR_MODEL		BR, ASCII	15		Manufacturers Model number, ASCII format
9Bh	MFR_REVISION		BR, ASCII	2		Manufacturers, revision number, ASCII format
9Ch	MFR_LOCATION		BR, ASCII	16	1	Manufacturers facility, ASCII format
9Dh	MFR_Data		BR	6	1	Manufacture date (YYYYMMDD)
9Eh	MFR_Serial		BR	13		Unit serial number, ASCII format.

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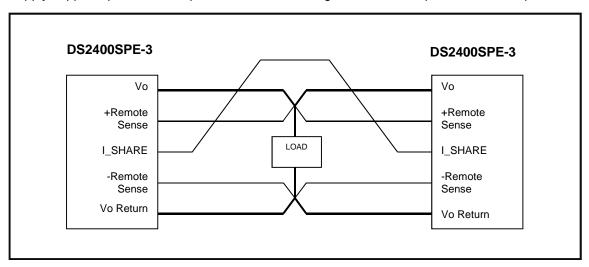
Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description
A0h	MFR_VIN_MIN	5A00	R	2	Linear	Minimum Input Voltage (90Vac)
A1h	MFR_VIN_MAX	0801	R	2	Linear	Maximum Input Voltage (264Vac)
A2h	MFR_IIN_MAX	00DA	R	2	Linear	Maximum Input Current (16A)
A3h	MFR_PIN_MAX	HL: A312 LL: 9011	R		Linear	Maximum Input Power (2700W for High Line and 1600W for Low Line)
A4h	MFR_VOUT_MIN	3317	R	2	Linear	Minimum Output Voltage. (11.6V)
A5h	MFR_VOUT_MAX	9A19	R	2	Linear	Maximum Output Voltage. (12.8V)
A6h	MFR_IOUT_MAX	HL: 13F3 LL: 96EB	R	2	Linear	Maximum Output Current (196.75A for high line and 114.75 for low line)
A7h	MFR_POUT_MAX	HL: 5812 LL: 5E11	R	2	Linear	Maximum Output Power (2400W for high line and 1400W for low line)
A8h	MFR_TAMBIENT_MAX	3700	R	2	Linear	Maximum Operating Ambient Temperature (Secondary Ambient) (55 degC)
A9h	MFR_TAMBIENT_MIN	0A00	R	2	Linear	Minimum Operating Ambient Temperature (Secondary Ambient) (10 degC)
AAh	MFR_EFFICIENCY_LL		BR	14		Sets or retrieves information about the efficiency of the device while operating at a low line condition. VInput:115V / P(L): 280W / E(L): 88% / P(M): 700W / E(M): 92% / P(H): 1400W / E(H): 91%
ABh	MFR_EFFICIENCY_HL		BR	14		Sets or retrieves information about the efficiency of the device while operating at a high line condition. VInput:230V / P(L): 480W / E(L): 93% / P(M): 1200W / E(M): 94% / P(H): 2400W / E(H): 91%

Application Notes

Current Sharing

The DS2400SPE series' main output V_0 is equipped with current sharing capability. When two or more power supplies are connected and operating in parallel, the sharing accuracy between units must be within the limits specified in the Table below.

The power supply support up to 4 units in parallel. Current sharing below 7% load per unit is not required.

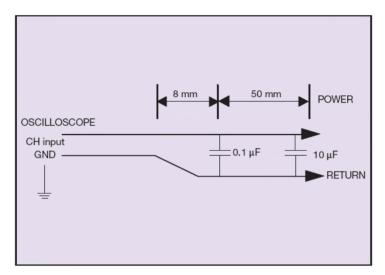


Current Sharing Accuracy

Load (per power supply unit)	Max Difference between PSUs
10% - 100%	8.0A
<10%	10.0A

Output Ripple and Noise Measurement

The setup outlined in the diagram below has been used for output voltage ripple and noise measurements on the DS2400SPE series. When measuring output ripple and noise, a scope jack in parallel with a $0.1\mu F$ ceramic chip capacitor, and a $10\mu F$ aluminum electrolytic capacitor should be used. Oscilloscope should be set to 20MHz bandwidth for this measurement.



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Record of Revision and Changes

Issue	Date	Description	Originators
1.0	08.13.2019	First Issue	E. Wang