DS1200HE

1200 Watts

Distributed Power System

Total Power: 1000-1200 Watts Input Voltage: 90 to 264 Vac # of Outputs: Single Main



- · 1200W output power
- High-power
- 1U X 2U power supply
- High-density design: 21.66 W / in³
- · Active power factor correction
- EN61000-3-2 harmonic compliance
- · Inrush current control
- · 80plus platinum efficiency
- N + 1 or N + N redundant
- · Hot plug operation
- · Active current sharing
- · Full digital control
- · PMBus compliant
- · Input power reporting
- Compatible with Artesyn's Universal PMBus GUI
- · Reverse airflow option
- · Two year warranty

Safety

UL/cUL 60950 (UL Recognized) NEMKO+ CB Report EN60950 CE Mark China CCC









Product Descriptions

The DS1200HE-3 series power supply features a very wide 90 to 264 Vac input voltage range and employ active power factor correction to minimize input harmonic current distortion and to ensure compliance with the international EN61000-3-2 standard – they have a power factor of 0.90 typical. The power supplies also feature active ac inrush control, to automatically limit inrush current at turn-on to 55 A maximum.

The power supply employs a new patent-pending ultra high efficiency conversion topology, together with an innovative power transformer and rectifier construction that further improves power density and reduces interconnect power losses. The power supply's main +12 Vdc payload output is digitally programmable over the range 11.4 to 12.6 Vdc, and users have a choice of standard I²C or advanced PMBus communications. The control software runs under Windows® on any standard PC, and uses a highly intuitive graphical user interface to simplify power supply set-up.

The DS1200HE-3 can deliver up to 100 A from its main +12 Vdc payload output, and up to 6 A from its +3.3 Vdc auxiliary output. The supply has a 1U x 2U form factor – it measures 11 x 3.2 inches, with a height of 1.57 inches – and has a power density of more than 21.66 watts per cubic inch. When fed with a 180 to 264 Vac input, the DS1200HE-3 can achieve a very high – 94 percent typical conversion efficiency at 50 percent full load.



Model Numbers

Standard	Output Voltage	Minimum Load	Maximum Load	Stand-By Supply	Air Flow Direction
DS1200HE-3	12.0Vdc	0A	100A	3.3V@6A	Normal (DC Connector to Handle)
DS1200HE-3-002	12.0Vdc	0A	100A	5V@4A	Normal (DC Connector to Handle)
DS1200HE-3-003	12.0Vdc	0A	100A	3.3V@6A	Reversed (Handle to DC Connector)
DS1200HE-3-004	12.0Vdc	0A	100A	5V@4A	Reversed (Handle to DC Connector)

Options

None

Electrical Specifications

Absolute Maximum Ratings

Stress in excess of those listed in the "Absolute Maximum Ratings" may cause permanent damage to the power supply. These are stress ratings only and functional operation of the unit is not implied at these or any other conditions above those given in the operational sections of this TRN. Exposure to any absolute maximum rated condition for extended periods may adversely affect the power supply's reliability.

Table 1. Absolute Maximum Ratings:

Parameter	Model	Symbol	Min	Тур	Max	Unit
Input Voltage: AC continuous operation	All models	V _{IN,AC}	90	-	264	Vac
Maximum Output Power (Main + Stand-by) V _{IN,AC} > 180Vac	All models	P _{O,max}	-	-	1200	W
Maximum Output Power (Main + Stand-by) V _{IN,AC} ≤ 180Vac	DS1200HE-3 DS1200HE-3-002 DS1200HE-3-003 DS1200HE-3-004	P _{O,max}	- - -	- - -	1000 1000 1200 1200	W
Isolation Voltage Input to outputs Input to safety ground Outputs to safety ground	All models All models All models				2121 2121 50	Vdc Vdc Vdc
Ambient Operating Temperature	All models	T _A	-10	-	+70 ¹	°C
Storage Temperature	All models	T _{STG}	-40	-	+85	°C
Humidity (non-condensing) Operating Non-operating	All models All models		10 10		90 95	% %
Altitude Operating Non-operating	All models All models		-		10,000 50,000	feet feet

Note 1 - With power derating (see page 23 power derating curve)

Input Specifications

Table 2. Input Specifications:

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Operating Input Voltage, AC		V _{IN,AC}	90	115/230	264	Vac _{RMS}
Input Vac Source Frequency		f _{IN,AC}	47	60	63	Hz
Maximum Input Current (I _O = I _{O,max} , I _{VSB} = I _{VSB,Max})	$V_{IN,AC} = 90V_{AC}$	I _{IN,max}	-	-	15	A _{RMS}
Standby Input Current (V _O Off, I _{VSB} = 0A)	$V_{IN,AC} = 90V_{AC}$ $V_{IN,AC} = 180V_{AC}$	I _{IN,standby}	-	-	200 300	mA _{RMS}
Standby Input Power (V _O Off, I _{VSB} = 0A)	All	P _{Standby}	-	-	4	W
No Load Input Current $(V_O On, I_O = 0A, I_{VSB} = 0A)$	$V_{IN,AC} = 90V_{AC}$ $V_{IN,AC} = 180V_{AC}$	I _{IN,no_load}	-	-	400 300	mA _{RMS}
Harmonic Line Currents	All	THD	Pe	r IEC1000-	3-2	
Power Factor	All		-	0.9	-	
Startup Surge Current (Inrush) @ 25°C	$V_{IN,AC} = 264V_{AC}$		-	-	55	A _{PK}
Input Fuse	Internal, L and N 5x20mm, Quick Acting 16A, 250V		-	-	16	А
Leakage Current to earth ground	$V_{IN,AC} = 240V_{AC}$ $f_{IN,AC} = 50/60 \text{ Hz}$		-	-	1.4	mA
PFC Switching Frequency	All	f _{SW,PFC}	90	-	110	KHz
Operating Efficiency @ 25°C	$I_{O} = 50\%I_{O,max}$ $V_{IN,AC} = 115V_{AC}$ $V_{IN,AC} = 230V_{AC}$	η	-	-	91.0 94.0	%
System Stability: Phase Margin Gain Margin			45 6	-	-	Ø dB

Output Specifications

Table 3. Output Specifications:

Parameter		Condition	Symbol	Min	Тур	Max	Unit
	All models		Vo	11.4	12.0	12.6	
Output Regulation	DS1200HE-3 DS1200HE-3-003	Inclusive of set-point, temperature change, warm-up drift and	V _{VSB}	2.97	3.30	3.63	v
	DS1200HE-3-002 DS1200HE-3-004	dynamic load	V _{VSB}	4.50	5.00	5.50	
	All models	Measure with a 0.1µF	Vo	-	-	120	
Output Ripple, pk-pk	DS1200HE-3 DS1200HE-3-003	ceramic capacitor in parallel with a 10µF	V _{VSB}	-	-	50	mV _{PK-PK}
	DS1200HE-3-002 DS1200HE-3-004	tantalum capacitor, 0 to 20MHz bandwidth	V _{VSB}	-	-	50	
	All models	V _{IN,AC} > 180Vac	I _O	0	-	100	
Output Current	DS1200HE-3 DS1200HE-3-002 DS1200HE-3-003 DS1200HE-3-004	V _{IN,AC} ≤ 180Vac	Io	0 0 0	- - -	83.3 83.3 100 100	A
·	DS1200HE-3 DS1200HE-3-003		I _{VSB}	0	-	6.0	
	DS1200HE-3-002 DS1200HE-3-004		I _{VSB}	0	-	4.0	
Ripple Switching Freque	ency	All	f _{SW,DC-DC}	560	-	580	KHz
V _O Current Share Accur	асу	40% to 100% I _{O,max} 10% to 40% I _{O,max}		-	-	5 20	%l _o
V _O Minimum Current Sh	are Loading			5	-	-	%I _{O,max}
Number of Parallel Units ¹		Main Output Current Share connected		-	-	4	Units
V _O Load Capacitance		Start up	-	2000	-	40000	μF
V _O Dynamic Response	Peak Deviation Settling Time	50% load change, slew rate = 1A/μs	±%V _O T _s	-	-	5 -	% mSec
V _O Long Term Stability Max change over 24 hou	urs	After thermal equilibrium (30 mins)	±%V _O	-	-	0.2	%

Note 1 - V_{SB} output do not use active current sharing. On paralleled units, maximum current on V_{SB} output rail should not exceed the current of one unit.

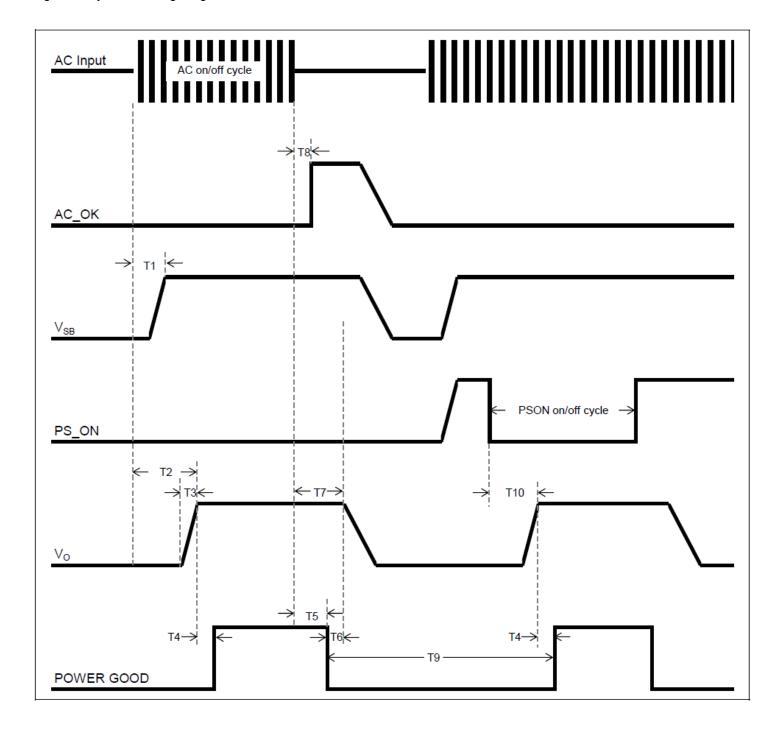
System Timing Specifications

Table 4. System Timing Specifications:

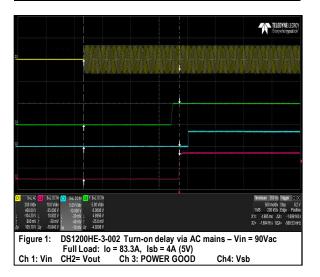
Label	Parameter		Тур	Max	Unit
T1	Delay from AC being applied to V _{SB} being within regulation	-	-	1700	mSec
T2	Delay from AC being applied to output voltages being within regulation with PSON_L asserted low.	-	-	2000	mSec
Т3	$V_{\rm O}$ rise time, 0V to $V_{\rm O}$ in regulation.	3	-	50	mSec
T4	Delay from output voltages within regulation limits to POWER GOOD asserted high.	100	-	1000	mSec
T5	Delay from loss of AC to de-assertion of POWER GOOD.	11	-	-	mSec
Т6	Delay from POWER GOOD de-asserted to output voltages dropping out of regulation limits.	1			mSec
Т7	Hold up time - time all output voltages, including V_{SB} , stay within regulation after loss of AC.	12	-	-	mSec
Т8	Delay from loss of AC input to ACOK_H going to low.	7	-	14	mSec
Т9	Duration of POWER GOOD being in the de-asserted state during an off/on cycle using AC or the PSON_L signal	100	-	-	mSec
T10	Delay from PSON_L active to output voltages within regulation limits.	-	-	350	mSec

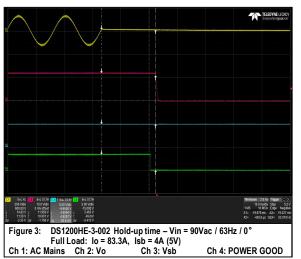
System Timing Specifications

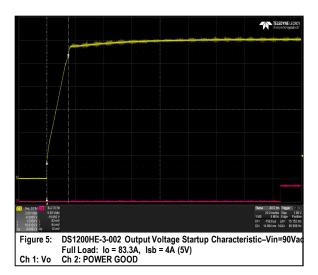
Figure 1. System Timing Diagram:

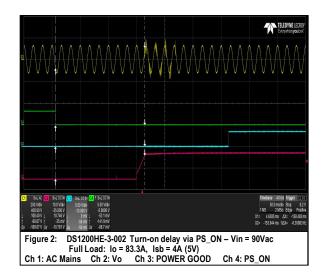


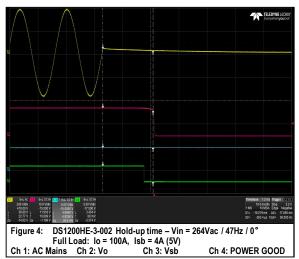
DS1200HE-3-002 Performance Curves

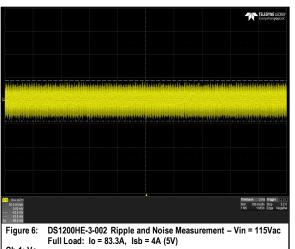




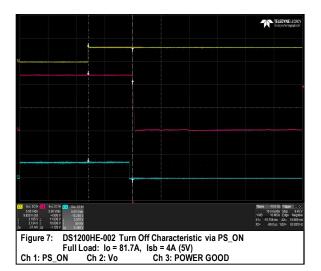


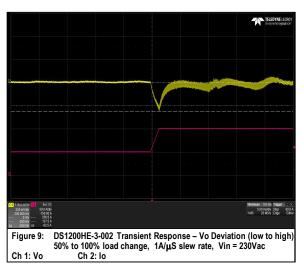


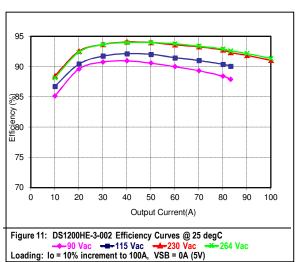


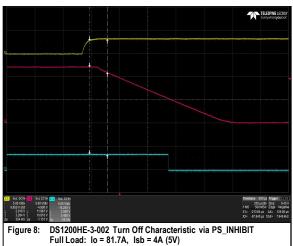


DS1200HE-3 Performance Curves



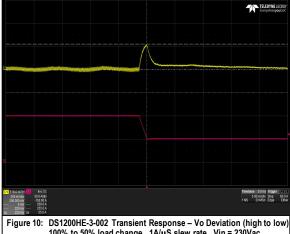






Full Load: lo = 81.7A, lsb = 4A (5V)

NHIBIT Ch 2: Vo Ch 3: POWER GOOD Ch 1: PS_INHIBIT Ch 2: Vo



100% to 50% load change, 1A/μS slew rate, Vin = 230Vac Ch 2: Io Ch 1: Vo

Protection Function Specification

Input Fusing

DS1200HE-3 series is equipped with an internal non user serviceable 16A High Rupturing Capacity (HRC) 250 Vac fuse to IEC 127 for fault protection in both the L1 and L2 lines input.

Over Voltage / Under Voltage Protection (OVP / UVP)

The power supply latches off during output overvoltage and under voltage with the AC line recycled to reset the latch.

OVP

Parameter	Min	Nom	Max	Unit
V _O Output Overvoltage	13.5	/	15	V
3.3V Standby Overvoltage	3.63	/	4.29	٧
5V Standby Overvoltage	5.5	/	6.5	V

UVP

Parameter	Min	Nom	Max	Unit
V _O Output Undervoltage	10.5	/	11.0	V

Over Current Protection (OCP)

DS1200HE-3 series includes internal current limit circuitry to prevent damage in the event of overload or short circuit. Recovery is automatic when the overload is removed, if the overload lasts for 1 second or less, and if it is between 120% to 130% of the rated load. If the overload is > 130% of rated load, the power supply will latch off immediately. In addition, if the overload fault is presented for longer than 1 second, the power supply will also latch off, requiring AC power or PSON_L recycling to restart the power supply.

Parameter	Input Voltage	Model	Min	Nom	Max	Unit
	180-264 Vac	All	120	/	150	Α
V _O Output Overcurrent	00.170.Vee	DS1200HE-3 DS1200HE-3-002	120	,	150	Δ.
Overcurrent	90-179 Vac	DS1200HE-3-003 DS1200HE-3-004	99.96	,	124.95	A
3.3V Standby Overcurrent	90-264 Vac	All	7.2	/	9	А
5V Standby Overcurrent	90-264 Vac	All	4.8	/	6	А

Technical Reference Note

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Short Circuit Protection (SCP)

The DS1200HE-3 series power supplies will withstand a continuous short circuit with no permanent damage, applied to its main output during start-up or while running. A short is defined as impedance less than 0.1 ohms.

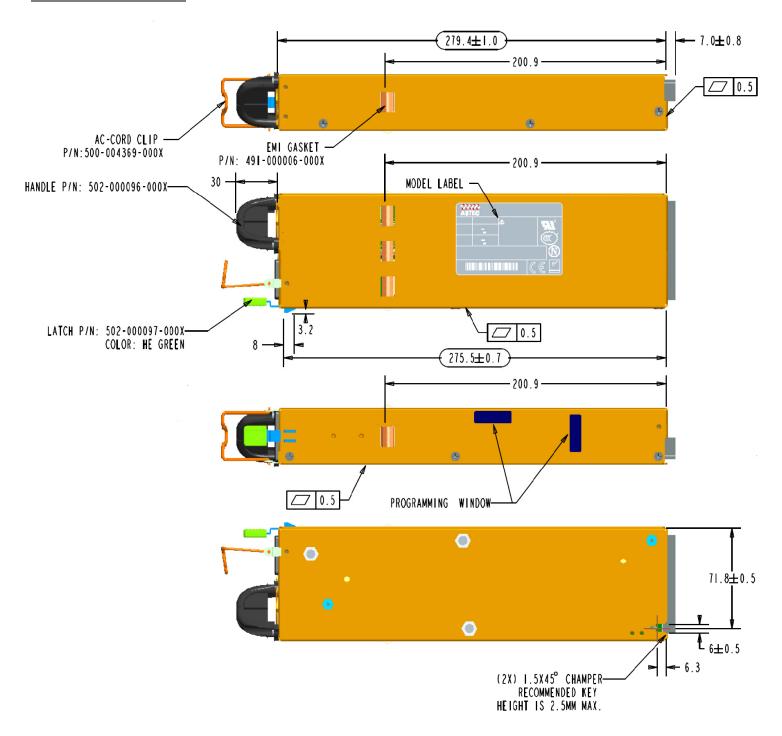
When the standby output V_{SB} is shorted the output will go into "hiccup mode". When the V_{SB} attempts to restart, the maximum peak current from the V_{SB} output will be less than 9.0A peak (3.3V) or 6.6A (5.0V). The maximum average current, taking into account the "hiccup" duty cycle, is less than 4.9A (3.3V) or 3.3A (5.0V).

Over Temperature Protection (OTP)

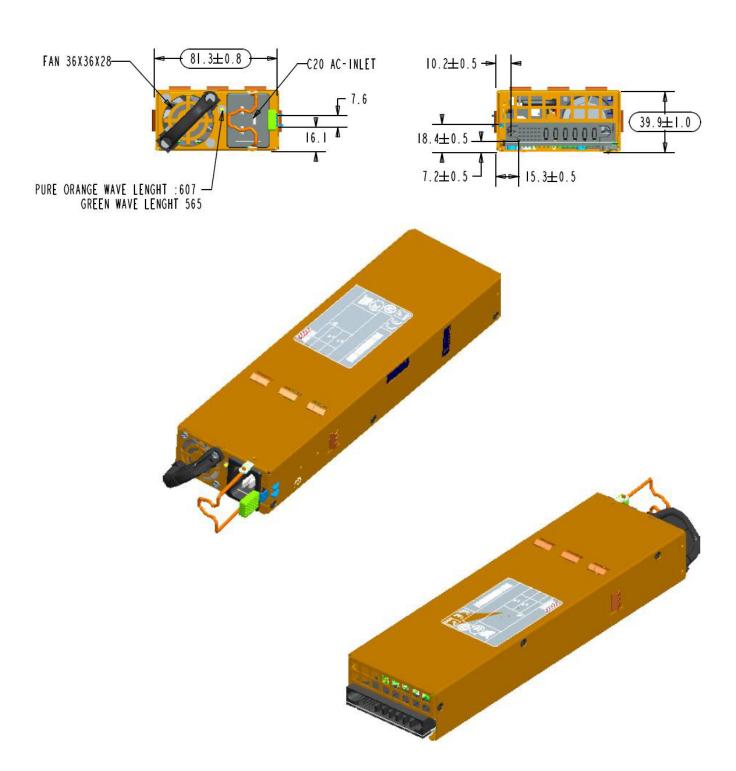
The power supply is internally protected against over temperature conditions. When the OTP circuit is activated, the power supply will latch off, requiring AC power or PS_ON recycling to restart the power supply.

Mechanical Specifications

Mechanical Outlines



Mechanical Outlines



Connector Definitions

AC Input Connector

Pin 1 L1 L2 Pin 2

Pin 3 Earth Ground

Output Connector - Power Blades

PB1 Main Output Return PB2 Main Output Return PB3 Main Output Return PB4 + Main Output (V_O) PB5 + Main Output (V_O) PB6 + Main Output (V_O)

Output Connector – Control Signals

PSON L Α1

A2 Main output remote sense return, **VSENSE-**

А3 Spare

Α4 - PS PRESENT

Α5 STAND-BY, +VSB

STAND-BY RETURN, -VSB A6

 ACOK_H (AC Input Present) B2 Main output remote sense, VSENSE+

ISHARE B3

B1

B4 PS INHIBIT/PSKILL

B5 - STAND-BY, +VSB

B6 STAND-BY RETURN, -VSB

C1 SDA (I²C Data Signal)

C2 SCL (I²C Clock Signal)

C3 - POWER GOOD/ PWOK H

C4 - Spare

C5 STAND-BY, +VSB

C6 STAND-BY RETURN, -VSB

 A0 (I²C Address BIT 0 Signal) D1

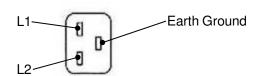
 A1 (I²C Address BIT 1 Signal) D2

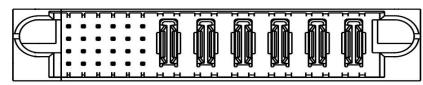
D3 PS INTERRUPT(Alarm)

 STAND-BY RMT SENSE, VSENSE_STBY D4

STAND-BY, +VSB D5

- STAND-BY RETURN, -VSB D6





View from power supply output connector end

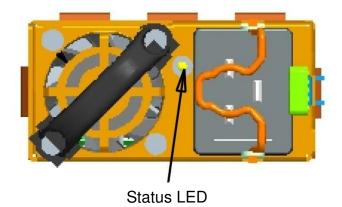
D1	D2	D3	D4	D5	D6						
C1	C2	СЗ	C4	C5	C6	PB1	PB2	DDa	DD4	PB5	DDG
В1	B2	ВЗ	B4	B5	B6	PBI	PB2	PB3	PB4	PBS	PB6
A1	A2	А3	A4	A5	A6						

Power / Signal Mating Connectors and Pin Types

Table 5. Mating Connectors for DS1200HE-3 series

Reference	On Power Supply	Mating Connector or Equivalent	
AC Input Connector	IEC320-C20	IEC320-C19	
	FCI Power Blade 51721 series 51721-10002406AA	FCI Power Blade 51741-10002406CC Straight Pins	
Output Connector	or Molex Power Connector SD-87667 series 87667-7002	FCI Power Blade 51761-10002406AALF Right Angle	
	0.00002	Any other approved equivalent	

LED indicator Definition



One bi-color (green/amber) LED at the power supply front provides status signal. The status LED conditions is shown on the below table.

Condition	LED Status
V _{SB} = ON, V _O = OFF, AC Input = ON	Blinking Green
$V_{SB} = ON, V_O = ON$	Solid Green
V _O = OVP / UVP	Blinking Amber
Power Supply Failure (OCP / OTP / FAN_FAULT)	Solid Amber

Technical Reference Note

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<u>Weight</u>

The DS1200HE-3 series weight is 2.95 lbs / 1.34 kg maximum.

Environmental Specifications

EMC Immunity

DS1200HE-3 series power supply is designed to meet the following EMC immunity specifications:

Table 6. Environmental Specifications:

Document	Description
FCC Part 15 Subpart J Class B/ EN55022, Level B	Conducted and Radiated EMI Limits
EN61000-3-2	Harmonics
EN61000-3-3	Voltage Fluctuations
IEC/EN 61000-4-2	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Electrostatic discharge immunity test. +/-15KV air, +/-8KV contact discharge, performance Criteria B
IEC/EN 61000-4-3	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Radiated, radio-frequency, 80-1000 MHz, 10V/m, AM 80% (1KHz), 900MHz, 10V/M, PM 100% (200Hz) electromagnetic field immunity test, Criteria A
IEC/EN 61000-4-4	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Electrical Fast Transient/Burst Immunity Test. 2KV for AC power port, 1.0KV for DC ports, I/O and signal ports performance Criteria B
IEC/EN 61000-4-5	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Surges. 2KV common mode and 1KV differential mode for AC ports and 0.5KV differential mode for DC power, I/O and signal ports, performance criteria B.
IEC/EN 61000-4-11	Electromagnetic Compatibility (EMC) - Testing and measurement techniques - Voltage Dips and Interruptions. 30% reduction for 500ms-Criteria B>95% reduction for 10mS, Criteria A, >95% reduction for 5000mS, Criteria C
EN55024	Information Technology Equipment-Immunity Characteristics, Limits and Method of Measurements

Safety Certifications

The DS1200HE-3 series power supply is intended for inclusion in other equipment and the installer must ensure that it is in compliance with all the requirements of the end application. This product is only for inclusion by professional installers within other equipment and must not be operated as a stand alone product.

Table 7. Safety Certifications for DS1200HE-3 series power supply system

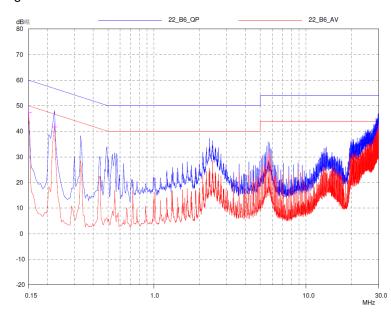
Document	File#	Description
UL 60950 No.	E132002-A314-UL - X1	US and Canada Requirements
CSA 22.2 No. 60950	E132002-A314-UL - X1	Information Technology Equipment - Safety - Part 1: General Requirements (Bi-National standard, with UL 60950-1)
EN60950	D-03011-M1	European Requirements
EN60950 Deviations		International Requirements
CB Certificate and Report	DK-35929-UL	(All CENELEC Countries)
CHINA CCC Approval	2013010907665315	China Requirements

EMI Emissions

The DS1200HE-3 series has been designed to comply with the Class B limits of EMI requirements of EN55022 FCC Part 15) and CISPR 22 (EN55022) for emissions and relevant sections of EN61000 (IEC 61000) for immunity. The unit is enclosed inside a metal box, tested at 1200W using resistive load with cooling fan.

Conducted Emissions

The applicable standard for conducted emissions is EN55022 (FCC Part 15). Conducted noise can appear as both differential mode and common mode noise currents. Differential mode noise is measured between the two input lines, with the major components occurring at the supply fundamental switching frequency and its harmonics. Common mode noise, a contributor to both radiated emissions and input conducted emissions, is measured between the input lines and system ground and can be broadband in nature.



The DS1200HE-3 power supplies have internal EMI filters to ensure the convertors' conducted EMI levels comply with EN55022 (FCC Part 15) Class B and EN55022 (CISPR 22) Class B limits. The EMI measurements are performed with resistive loads at maximum rated loading.

Sample of EN55022 Conducted EMI Measurement at 115Vac input

Note: Blue Line refers to Artesyn Quasi Peak margin, which is 3dB below the CISPR international limit. Red Line refers to the Artesyn Average margin, which is 3dB below the CISPR international limit.

Table 8. Conducted EMI emission specifications of the DS1200HE-3 series

Parameter	Model	Symbol	Min	Тур	Max	Unit
FCC Part 15, class B	All	Margin	-	-	3	dB
CISPR 22 (EN55022) class B	All	Margin	-	-	3	dB

Technical Reference Note

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Radiated Emissions

Unlike conducted EMI, radiated EMI performance in a system environment may differ drastically from that in a stand-alone power supply. The shielding effect provided by the system enclosure may bring the EMI level from Class A to Class B. It is thus recommended that radiated EMI be evaluated in a system environment. The applicable standard is EN55022 Class B (FCC Part 15). Testing ac-dc convertors as a stand-alone component to the exact requirements of EN55022 can be difficult, because the standard calls for 1m leads to be attached to the input and outputs and aligned such as to maximize the disturbance. In such a set-up, it is possible to form a perfect dipole antenna that very few ac-dc convertors could pass. However, the standard also states that an attempt should be made to maximize the disturbance consistent with the typical application by varying the configuration of the test sample.

Operating Temperature

The DS1200HE-3 series power supply will start and operate within stated specifications at an ambient temperature from - 10 °C to 50 °C under all load conditions with internal fan. All models can operate up to 70 °C with derated power.

Forced Air Cooling

The DS1200HE-3 series power supply included internal cooling fans as part of the power supply assembly to provide forced air-cooling to maintain and control temperature of devices and ambient temperature in the power supply to appropriate levels. The standard direction of airflow is from the DC connector end to the AC connector end of the power supply.

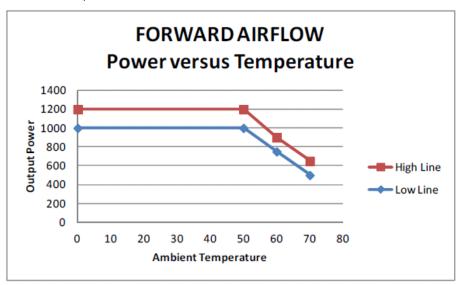
The cooling fan is a variable speed fan. The fan speed is controlled by the PWM duty cycle of the fan supply voltage depending on the main output 12V load condition per below table:

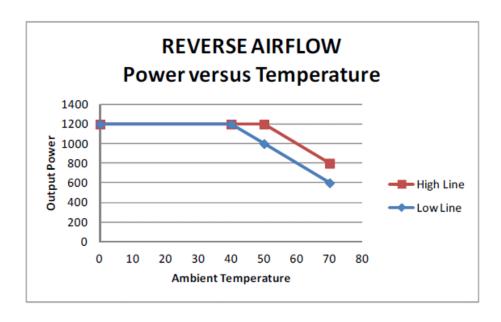
For DWM Duty Cycle	Main Output (12V) Load (A)				
Fan PWM Duty Cycle	110 Vac	230 Vac			
100%	81.6	98.4			
77%	75.5	93.5			
73%	69.4	88.5			
62%	63.2	83.6			
54%	57.1	78.7			
41%	51.0	73.8			
38%	44.88 and below	68.9 and below			
38%	Stand-By Mode	Stand-By Mode			

Power Derating Curves

DS1200HE-3 and DS1200HE-3-002 can operate up to a maximum ambient temperature of 70 $^{\circ}$ C with derating. Power derating starts when ambient reaches 50 $^{\circ}$ C. Up to 70 $^{\circ}$ C, nominal power reduced to 600W for high line and 500W for low line.

DS1200HE-3-003 and DS1200HE-3-004 can operate up to a maximum ambient temperature of 70 °C with derating. Power derating starts when ambient reaches 50 °C for high line and 40 °C for low line. Up to 70 °C, nominal power reduced to 800W for high line. Up to 70 °C, nominal power reduced to 600W for low line (shown in the Power Derating Curves below).





Storage and Shipping Temperature / Humidity

The DS1200HE-3 series power supply can be stored or shipped at temperatures between -40 $^{\circ}$ C to +85 $^{\circ}$ C and relative humidity from 10% to 95% non-condensing.

Altitude

The DS1200HE-3 series will operate within specifications at altitudes up to 10,000 feet above sea level. The power supply shall not be damaged when stored at altitudes of up to 50,000 feet above sea level.

Humidity

The DS1200HE-3 series will operate within specifications when subjected to a relative humidity from 10% to 90% non-condensing. The DS1200HE-3 series can be stored in a relative humidity from 10% to 95% non-condensing.

Vibration

The DS1200HE-3 series power supply will pass the following vibration specifications:

Non-Operating Random Vibration

Acceleration	1.87	gRMS		
Frequency Range	10-500		Hz	
Duration	30	mins		
Direction	3 mutually perpendicular axis			
PSD Profile	FREQ 10 Hz 200 Hz 500 Hz	SLOPE <u>dB/oct</u> -2.66 	PSD g²/Hz 0.009 0.009 0.004	

Operating Random Vibration

Acceleration	0.153		gRMS	
Frequency Range	5-100		Hz	
Duration	30		mins	
Direction	3 mutually perpendicular axis			
PSD Profile	FREQ 5 Hz 10-50 Hz 100 Hz	SLOPE <u>dB/oct</u> 11 -10	PSD <u>g²/Hz</u> 0.00003 0.0004 0.00003	

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Shock

The DS1200HE-3 series power supply will pass the following vibration specifications:

Non-Operating Half-Sine Shock

Acceleration	30	G
Duration	18	msec
Pulse	Half-Sine	
No. of Shock	3 shocks on each of 6 faces	

Operating Half-Sine Shock

Acceleration	4	G
Duration	22	msec
Pulse	Half-Sine	
No. of Shock	3 shocks on each of 6 faces	

Power and Control Signal Descriptions

AC Input Connector

This connector supplies the AC Mains to the DS1200HE-3 power supply.

Pin 1 - L1

Pin 2 - L2

Pin 3 - Earth Ground

<u>Output Connector – Power Blades</u>

These pins provide the main output for the DS1200HE-3. The + Main Output (V_O) and the Main Output Return pins are the positive and negative rails, respectively, of the V_O main output of the DS1200HE-3 power supply. The Main Output (V_O) is electrically isolated from the power supply chassis.

PB1 - Main Output Return

PB2 - Main Output Return

PB3 - Main Output Return

PB4 - + Main Output (V_O)

PB5 - + Main Output (V_O)

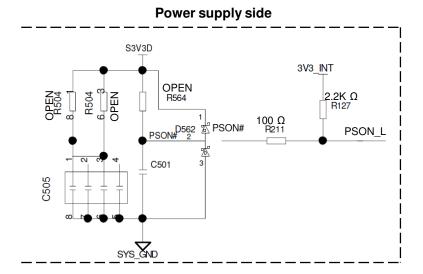
PB6 - + Main Output (V_O)

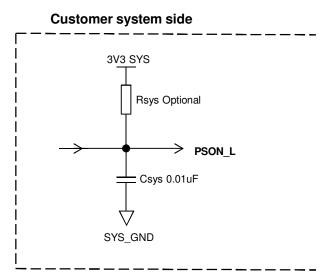
Output Connector - Control Signals

The DS1200HE-3 series contains a 24 pins control signal header providing an analogue control interface, standby power and I²C interface signal connections.

PSON_L - (pin A1)

This signal input pin controls the normal turning ON and Off of the Main Output of the DS1200HE-3 power supply. The power supply main output (V_O) will be enabled when this signal is pulled low, below 0.8 V. The Power supply output (except V_{SR} output) will be disabled when this input is driven higher than 3.3V, or left open circuited.





VSENSE-, VSENSE+-(pins A2, B2)

The main output of the DS1200HE-3 is equipped with a Remote Sensing capability that will compensate for a power path drop around the entire loop of 400 mV. This feature is implemented by connecting the VSENSE+ (pin B2) and the VSENSE- (pin A2) to the positive and negative rails of the main output, respectively, at a location that is near to the load. Care should be taken in the routing of the sense lines as any noise sources or additional filtering components introduced into the voltage rail may affect the stability of the power supply. The DS1200HE-3 will operate appropriately without the sense lines connected; however it is recommended that the sense lines be connected directly to the main output terminals if remote sensing is not required. This remote sense circuit will not raise the power supply's output voltage to the OVP trip level. Main Output Remote Sense has no effect on the Standby Output (V_{SB}).

PS_PRESENT - (pin A4)

This signal pin is connected to Main Output Return inside the power supply via a 220 Ohm resistor. This pin is to be pull high on the system side by a resistor of 4.7K or higher. A TTL logic LOW indicates the power supply is inserted and seated into the system power supply connector. A Logic HIGH indicated the removal of the power supply.

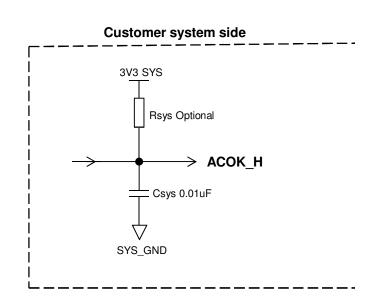
STAND-BY, STAND-BY RETURN – (pins A5, A6, B5, B6, C5, C6, D5, D6)

The DS1200HE-3 provides a regulated 3.3 volt 6 amp (or 5.0 volt 4 amp) auxiliary output voltage to power critical circuitry that must remain active regardless of the on/off status of the power supply's main output. The Standby Output (V_{SB}) voltage is available whenever a valid AC input voltage is applied to the unit. The StandBy Output is independently short circuit protected and is referenced to the StandBy Output Return pins (A6, B6, C6, D6).

ACOK_H - (pin B1)

The ACOK_H signal is a normally LOW level TTL logic signal when the AC input voltage is within the allowable limits. A TTL logic HIGH level, with a 5mS early warning will be sent before the main output loses regulation. This signal is a common drain output internally pulled up in the power supply to StandBy Output via a 1K ohm resistor. It is capable of driving the output below 0.4V with a load of 10mA.

Power supply side 3V3_INT R10 1Kohm ACOK R70 13Kohm R72 C1 13Kohm SYS_GND



ISHARE- (pin B3)

The DS1200HE-3 supports active current sharing through a single wire connection between the power supplies. This input/output signal pin allows two or more power supplies to share the main output load current to increase the overall power capability or to operate the units in a N+1 configuration for redundancy purposes.

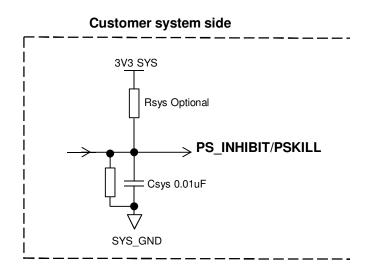
The voltage of this signal will be a linear slope from no load to full load. At 50A, the output of the ISHARE pin will be between 3.65 and 4.35V.

When two or more power supplies are connected and operating in parallel and each is delivering 40-50% of its rated output to the load, the power supplies will current share within 5% accuracy. When supplying light loads between 10% and 30% of its rated load, the power supplies will share within 20% accuracy. (Below 10% load, there is no guarantee of output current sharing). If any power supply is hot swapped, no glitch will occur that violates the regulation limits of the power supply defined in this specification.

PS_INHIBIT / PSKILL - (pin B4)

This signal is used for fast output turn off on PSU, normally pulled to logic low (Isink – 3mA max) or connected to standby return on PSU mating connector. When left open power supply operation will be inhibited. This feature minimizes arching / damage to output connector power pins during PSU removal from system chassis. This signal pin is internally pulled-up through 2K7 resistor to 3.3V_internal supply.

S3V3D R513 2K7ohm SYS_GND



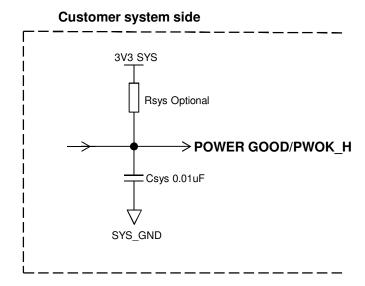
SDA, SCL and PS_INTERRUPT - (pin C1, C2, D3)

Please refer to "Communication Bus Descriptions" section.

POWER GOOD/PWOK_H – (pin C3)

The POPWER GOOD/PWOK is an output signal driven high, by the power supply to indicate that all outputs are valid. If any of the power supply outputs fails below its regulation limits, this output will be driven low. The output signal is a common drain output internally pulled up in the power supply to internal standby supply (anode side of StandBy Output OR'ing circuit) via a 1K ohm resistor. It is capable of driving the output below 0.4V with a load of 6mA.

Power supply side 3V3_INT R43 1Kohm DCOK_PWOK R68 IN SYS_GND



A0, A1 - (pins D1, D2)

Please refer to "Communication Bus Descriptions" section.

VSENSE_STBY - (pin D4)

Power supply will employ remote sense capability for the stand-by output for positive rail for a compensation of 50 mV max. This pin shall be connected as close to the loading as possible, or connected to the stand-by output at the base of the output connector if not used. If left open, the remote sense does not work properly and the voltage level of the stand-by output will go lower than guaranteed spec.

Communication Bus Descriptions

I²C Bus Signals

The DS1200HE-3 power supply contains enhanced monitor and control functions implemented via the I²C bus. The DS1200HE-3 I²C functionality (PMBusTM and FRU data) can be accessed via the output connector control signals. The communication bus is powered either by the internal 3.3V supply or from an external power source connected to the StandBy Output (ie: accessing an unpowered power supply as long as the StandBy Output of another power supply connected in parallel is on).

If units are connected in parallel or in redundant mode, the StandBy Outputs must be connected together in the system. Otherwise, the I²C bus will not work properly when a unit is inserted into the system without the AC source connected.

Note: PMBus[™] functionality can be accessed only when the PSU is powered-up.

Guaranteed communication I²C speed is 100KHz.

SDA, SCL (I²C Data and Clock Signals) – (pin C1, C2)

I²C serial data and clock bus - these pins must be pull-up in the system by an 1K ohm resistor to the StandBy Output.

PS_INTERRUPT - (pin D3)

PS_INTERRUPT is used to send a signal to the system that a fault in the power supply occurred. This signal is normally logic level HIGH. It will go to a LOW logic level when a fault bit has been set in the power supply's status register. To reset the PS_INTERRUPT signal back to normal (logic HIGH level) - (1) recycle input AC power, (2) toggle PSON signal and (3) issuance of a CLEAR FAULTS PMBusTM command.

A0, A1 (I²C Address BIT 0, BIT1 Signals) – (pin D1, D2)

These two input pins are the address lines A0 and A1 to indicate the slot position the power supply occupies in the power bay and define the power supply addresses for FRU data and PMBus™ data communication. This allows the system to assign different addresses for each power supply. During I²C communication between system and power supplies, the system will be the master and power supplies will be slave.

They are internally pulled up to internal 3.3V supply with a 1K resistor.

I²C Bus Communication Interval

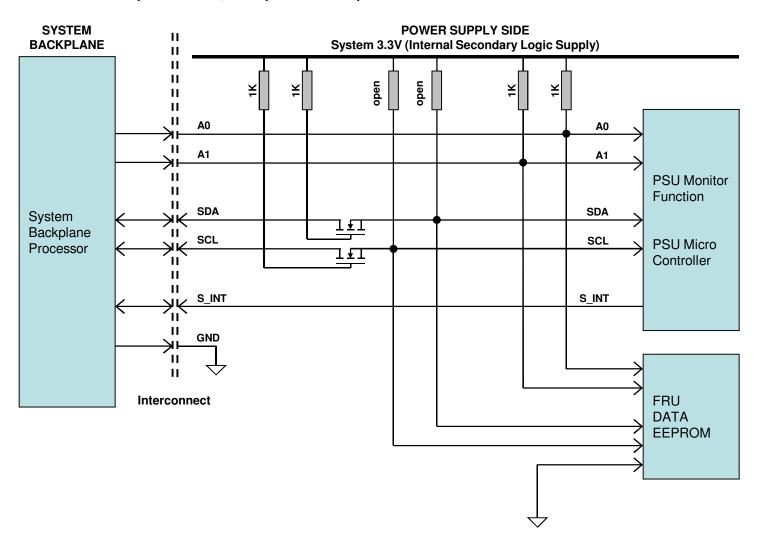
The interval between two consecutive I²C communications to the power supply should be at least 50ms to ensure proper monitoring functionality.

I²C Bus Signal Integrity

The noise on the I²C bus (SDA, SCL lines) due to the power supply will be less than 500mV peak-to-peak. This noise measurement should be made with an oscilloscope bandwidth limited to 100MHz. Measurements should be make at the power supply output connector with 3.2K ohm resistors pulled up to StandBy Output and 20pf ceramic capacitors to StandBy Output Return.

The noise on the address lines A0 and A1 will be less than 100mV peak-to-peak. This noise measurement should be made at the power supply output connector.

I²C Bus Internal Implementation, Pull-ups and Bus Capacitances



I²C Bus - Recommended external pull-ups:

Electrical and Interface specifications of I^2C signals (referenced to StandBy Output Return pin, unless otherwise indicated):

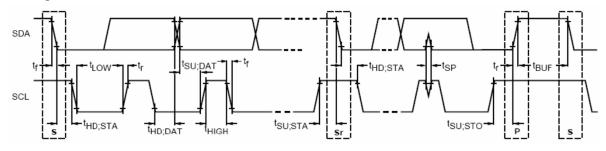
Parameter	Condition	Symbol	Min	Тур	Max	Unit
SDA, SCL internal pull-up resistor		R _{int}	-	-	-	Kohm
SDA, SCL internal bus capacitance		C _{int}	-	0	-	pF
Recommended external pull-up resistor	1 PSU	R _{ext}	-	1.0	-	Kohm
1 PSU	4 PSU		-	0.25	-	Kohm

Logic Levels

DS1200HE-3 series power supply I2C Communication Bus will respond to logic levels as per below:

Logic High: 5.1V Nominal (Specs is 2.1V to 5.5V)* Logic Low: 500mV nominal (Specs is 800mV max)* *Note: Artesyn 73-769-001 I2C adapter was used.

Timings



Davamatav	Cumbal	Standard-I	Mode Soecs	Act		I Imia
Parameter	Symbol	Min	Max	AC	tual	Unit
SCL Clock Frequency	f _{SCL}	0	100	100		kHz
Hold time (repeated) START condition	t _{HD;STA}	4.0	-	4.41		us
LOW period of SCL clock	t _{LOW}	4.7	-	15	5.9	us
HIGH period of SCL clock	t _{HIGH}	4.0	-	2.86		us
Setup time for repeated START condition	t _{SU;STA}	4.7	-	3.565		us
Data hold time	t _{HD;DAT}	0	3.45	0.2008		us
Data setup time	t _{SU;DAT}	250	-	26	880	ns
Rise time	t _r	-	1000	SCL = 1.078	SDA = 909	ns
Fall time	t _f	-	300	SCL = 130	SDA = 122	ns
Setup time for STOP condition	t _{SU;STO}	4.0	-	5.76		us
Bus free time between a STOP and START condition	t _{BUF}	4.7	-	30).7	ms

Device Addressing

The DS1200HE-3 series will respond to supported commands on the I^2C^{TM} bus that are addressed according to pins A1 and A0 pins of output connector.

Address pins are held HIGH by default via pulled up to internal 3.3V supply with a 1K resistor. To set the address as "0", the corresponding address line should be pulled down to logic ground level. Below tables show the address of the power supply with A0 and A1 pins set to either "0" or "1".:

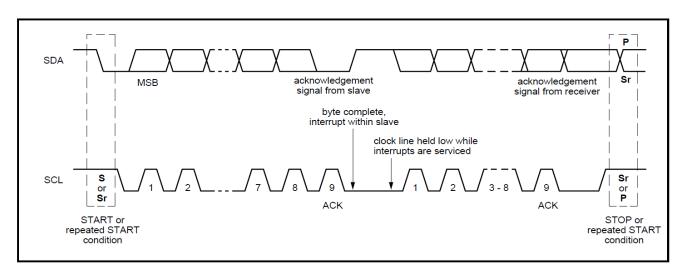
PSU Slot	Slot ID Bits		PMBus™ Address	EEPROM (FRU)	
P50 510t	A 1	Α0	PMBus' Address	Read Address	
1	0	0	0xB0h	0xB0h	
2	0	1	0xB2h	0xB2h	
3	1	0	0xB4h	0xB4h	
4	1	1	0xB6h	0xB6h	

^{*} Default PMBus™ address when A0 and A1 are left open

I²C Clock Synchronization

The DS1200HE-3 series power supply might apply clock stretching. An addressed slave power supply may hold the clock line (SCL) low after receiving (or sending) a byte, indicating that it is not yet ready to process more data. The system master that is communicating with the power supply will attempt to raise the clock to transfer the next bit, but must verify that the clock line was actually raised. If the power supply is clock stretching, the clock line will still be low (because the connections are open-drain).

The maximum time out condition for clock stretching for DS1200HE-3 is 100 milliseconds. The maximum low timeout condition for clock stretching is 25 milliseconds.



Power Supply Status Register, PMBus™ Register 0x79h

Power supply status monitoring can be done via the PMBus[™] register 0x79h or as I/O expander Detailed explanation of functions is given below:

Upper Byte

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
Vout	lout/Pout	Input	MFR	Power Good	Fan	Other	Unknown

Lower Byte

BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
Busy	OFF	Vout_OV	lout_OC	Vin_UV	Temperature	CML	None of the above

Upper Byte

Vout -Output Voltage Status

-This bit will be set high when fault has been triggered on main output.

Iout/Pout - Output Current/Output Power Status

-This bit will be set high when fault has been triggered on lout/Pout.

Input -Input Voltage Status

-This bit will be set high when fault has been triggered on Input voltage.

MFR - Manufacturer Status

- This bit will be set high when fault has been triggered on Manufacturer defined fault.

· Power Good - Power Good status

- This bit will be set high when fault has been triggered on Manufacturer defined fault.

Fan -Fan status

-This bit will be set high when fault has been triggered on Fan control.

Other - Not usedUnknown - Not used

Lower Byte

Busy
 This bit will be set high when the receiving device is too busy to respond on the communication on the bus

Off - Not used

Vout_OV - Over Voltage Protection

- This bit will be set high when fault has been triggered on main output

lout OC - Over Current Protection

- This bit will be set high when fault has been triggered on output load

Vin_UV - Under Voltage Protection

- This bit will be set high when input under-voltage occur

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- Temperature Over Temperature Protection
 - This bit will be set high when OTP is triggered
- CML
- Communication, Memory or Logic Fault
- This bit will be set high when memory or logic fault has occurred
- None of the Above This bit will be set high when a fault triggered is not listed above

Note: Register 0x79h gives the general status of the PSU but for specific area of interest, assigned registers should read. Details are given below Status Register Code.

Status Register Code		
Signal Name	Code (Binary)	Code (Hex)
Status_Vout	01111010	7A
Status_lout	01111011	7B
Status_Input	01111100	7C
Status_Temperature	01111101	7D
Status_CML	01111110	7E
Status_Other	01111111	7F
Status_MFR_Specific	10000000	80
Status_Fans_1_2	10000001	81
Status_Fans_3_4	10000010	82

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FRU (EEPROM) Data

The FRU (Field Replaceable Unit) data format is compliant with the Intel IPMI v1.0 specification.

The DS1200HE-3 uses 1 page of EEPROM for FRU purpose. A page of EEPROM contains up to 256 byte-sized data locations.

Where: OFFSET

-The OFFSET denotes the address in decimal format of a particular data byte within

DS1200HE-3 EEPROM.

SPEC VALUE - The VALUE details data written to a particular memory location of the EEPROM.

DEFINITION - The contents DEFINITION refers to the definition of a particular data byte.

OFF	SET	DEFINITION	SPEC VALUE		
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)	
0	00	Format Version Number (Common Header)	1	01	
1	01	Internal Use Area Starting Offset	10	0A	
2	02	Chassis Info Area Starting Offset	0	00	
3	03	Board Info Area Starting Offset	1	01	
4	04	Product Info Area Starting Offset	0	00	
5	05	Multi-Record Area Starting Offset	13	0D	
6	06	Reserved	0	00	
7	07	Common Header Checksum	231	E7	
8	08	Format Version Number (Board Info)	1	01	
9	09	Board Info Area Length	72	48	
10	0A	Language Code	0	00	
11	0B	Manufacturing Date/Time	0	00	
12	0C		0	00	
13	0D		0	00	
14	0E	Board Manufacturer Type/Length	3	03	
15	0F	Board Manufacturer	48	30	
16	10		211	D3	
17	11		164	A4	
18	12	Board Product Name Type/Length	31	1E	
19	13 14	Board Product Name, 30 Byte sequence "PWR SPLY,1200W,DS1200HE-3"	80 87	50 57	
20 21	15	In Decimal = 080, 087, 082, 032, 083, 080, 076, 089, 044, 049, 050, 048, 048, 087, 044, 068, 083,	87 82	57 52	
22	16	049, 050, 048, 048, 072, 069, 045, 051, 032, 032, 032, 032, 032, 032	32	20	
23	17	In Hex = 50H, 57H, 52H, 20H, 53H, 50H, 4CH, 59H, 2CH, 31H, 32H, 30H, 30H, 57H, 2CH, 44H,	83	53	
24	18	53H, 31H, 32H, 30H, 30H, 48H, 45H, 2DH, 33H, 20H, 20H, 20H, 20H, 20H	80	50	
25	19	55.1, 5.1.1, 52.1, 53.1, 53.1, 15.1, 12.1, 53.1, 23.1, 23.1, 23.1, 23.1, 23.1	76	4C	
26	1A		89	59	
27	1B		44	2C	
28	1C		49	31	
29	1D		50	32	
30	1E		48	30	
31	1F		48	30	
32	20		87	57	
33	21		44	2C	
34	22		68	44	
35	23		83	53	
36	24		49	31	
37	25		50	32	
38	26		48	30	
39	27		48	30	

(DEC)	ET	DEFINITION	SPEC VALUE		
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)	
40	28		72	48	
41	29		69	45	
42	2A		45	2D	
43	2B		51	33	
44	2C		32	20	
45	2D		32	20	
46	2E		32	20	
47	2F		32	20	
48	30		32	20	
49	31	Board Serial Number Type/Length	14	0E	
50	32	Board Serial Number	67	43	
51	33		67	43	
52 53	34 35		77 77	4D 4D	
54	36		77	4D 4D	
55	37		77	4D 4D	
56	38		77	4D	
57	39		84	54	
58	3A		84	54	
59	3B		84	54	
60	3C		88	58	
61	3D		88	58	
62	3E		88	58	
63	3F		88	58	
64	40	Board Part Number Type/Length	9	09	
65	41	Board Part Number	80	50	
66	42		80	50	
67	43		80	50	
68	44		80	50	
69	45		80	50	
70	46		80	50	
71	47		82	52	
72	48		82	52	
73	49		82	52	
74	4A	FRU File ID Type/Length	1	01	
75	4B	FRU File ID Data	1	01	
		01h for DS1200HE-3 platform			
		02h for DS1200HE-3-002 platform			
		03h for DS1200HE-3-003 platform			
		04h for DS1200HE-3-004 platform			
76	4C	Type/Length	3	03	
77	4D	Reserved	0	00	
78	4E		0	00	
79	4F	Board Area Checksum	240	E0	
80	50	Internal Use Area Format Version	1	01	
81	51	CDE format Presence	67	43	
82	52		68	44	
83	53		69	45	
84	54		32	20	
85	55	Board Info Area Part Number Checksum	0	00	
86	56	Chassis Info Area Part Number Checksum	0	00	
87	57	Product Info Area Part Number Checksum	0	00	
		Header Devision and Flore	4	0.1	
88	58	Header Revision and Flags	1	01	

OFFSET		DEFINITION	SPEC VALUE		
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)	
90	5A	Units	0	00	
91	5B	EEPROM Size	0	00	
92	5C		1	01	
93	5D	Header Length	15	0F	
94	5E	Header Checksum	213	D5	
95	5F	Element Count	2	02	
96	60	Element Type #1	4	04	
97	61	Element Offset #1	133	85	
98	62		0	00	
99	63	Element Type #2	7	07	
100	64	Element Offset #2	154	9A	
101	65		0	00	
102	66	Reserved	0	00	
103	67		0	00	
104	68	Record Type ID	0	00	
105	69	Record Info	130	82	
106	6A	Record Data Length	24	18	
107	6B	Record Data Checksum	254	FE	
108	6C	Record Header Checksum	80	50	
109	6D	Overall Capacity	176	B0	
110	6E		4	04	
111	6F	Peak VA	160	A0	
112	70		5	05	
113	71	Inrush Current	40	28	
114	72	Inrush Interval in ms	10	0A	
115	73	Low End Input Voltage Range 1	40	28	
116	74		35	23	
117 118	75 76	High End Input Voltage Range 1	32 103	20 67	
119	77	Low End Input Voltage Range 2	0	00	
120	77	Low End input voltage hange 2	0	00	
121	79	High End Input Voltage Range 2	0	00	
122	7A	Thigh End input voltage runge E	ő	00	
123	7B	Low End Input Frequency Range	47	2F	
124	7C	High End Input Frequency Range	63	3F	
125	7D	A/C Dropout Tolerance	10	0A	
126	7E	Binary Flags	47	2F	
120	'-	Feature Flags.	, T		
		Bit(s) Meaning			
		7:6 Reserved, write as 00b			
		5 PMBUS capable or not. 1 if Supported 0 if not.			
		4 Tachometer Pulses per Rotation/Predictive Fail Polarity. See Error! Reference source not found. for more information.			
		3 Hot Swap Support. Identifies whether (1b) or not (0b) the power.			
		2 Auto-switch. Identifies whether (1b) or not (0b) the power supply supports auto-switch, and			
		provides additional meaning to the Input Voltage Range fields of this record.			
		1 Power Factor Correction. Identifies whether (1b) or not (0b) the power supply supports PFC. 0 Predictive Fail Support. Identifies whether (1b) or not (0b) the power supply supports the			
		predictive fail pin. See also section.			
127	7F	Peak Wattage	255	FF	
128	80		255	FF	
-		!			

OFFSET		DEFINITION	SPEC VALUE		
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)	
129	81	Combined Wattage	0	00	
130	82		0	00	
131	83		0	00	
132	84	Predictive Fail Tachometer Lower Threshold	0	00	
133	85	Element Type #1	4	04	
134	86	Element Length #1	28	1C	
135	87		0	00	
136	88	Element Checksum #1	216	D8	
137	89	Service Tag Length	7	07	
138	8A	Service Tag	0	00	
139	8B	ocivide rug	0	00	
140	8C		0	00	
141	8D		0	00	
142	8E		0	00	
143	8F		0	00	
144	90		0	00	
145	91	Related Service Tag Count	1	01	
146	92	Related Service Tags	0	00	
147	93	Total Co. No. 14gc	Ö	00	
148	94		0	00	
149	95		Ö	00	
150	96		Ö	00	
151	97		0	00	
151	98		0	00	
153	99	Element Type #2	0	00	
154	9A	Element Length #2	20	14	
155	9B		0	00	
156	9C	Element Checksum #2	216	D8	
157	9D	Asset Tag Length	15	0A	
158	9E	Asset Tag	15	0A	
159	9F		0	00	
160	Α0		0	00	
161	A1		0	00	
162	A2		0	00	
163	A3		0	00	
164	A4		0	00	
165	A5		0	00	
166 167	A6 A7		0 0	00 00	
168	A7 A8	Unused Area	0	00	
169	A9	0.14004 7.104	0	00	
170	AA		0	00	
171	AB		0	00	
172	AC		0	00	
173	AD		0	00	
174	AE		0	00	
175	AF		0	00	
176	B0		Ö	00	
177	B1		0	00	
178	B2		Ö	00	
179	B3		0	00	
180	B4		0	00	
181	B5		0	00	
182	В6		0	00	

OFF	SET	DEFINITION	SPEC Y	VALUE
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
183	B7	Unused Area	0	00
184	B8		0	00
185	B9		0	00
186	BA		0	00
187	BB		0	00
188	BC		0	00
189	BD		0	00
190	BE		0	00
191	BF		0	00
192	C0		0	00
193	C1		0	00
194	C2		0	00
195	C3		0	00
196 197	C4 C5		0 0	00 00
198	C6		0	00
199	C7		0	00
200	C8		0	00
201	C9		0	00
202	CA		0	00
203	СВ		0	00
204	CC		0	00
205	CD		0	00
206	CE		0	00
207	CF		0	00
208	D0		0	00
209	D1		0	00
210	D2		0	00
211	D3		0	00
212	D4		0	00
213	D5		0	00
214	D6		0	00
215	D7		0	00
216 217	D8 D9		0	00 00
217	D9 DA		0 0	00
219	DB		0	00
220	DC		0	00
221	DD		0	00
222	DE		0	00
223	DF		0	00
224	E0		0	00
225	E1		0	00
226	E2		0	00
227	E3		0	00
228	E4		0	00
229	E5		0	00
230	E6		0	00
231	E7		0	00
232	E8		0	00
233	E9		0	00
234	EA		0	00
235	EB		0	00
236	EC		0	00
237	ED		0	00
238	EE		0	00
239 240	EF F0		0	00 00
240	F0 F1		0 0	00
24 1	l LI		U	UU

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OFF	SET	DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
242	F2	Unused Area	0	00
243	F3		0	00
244	F4		0	00
245	F5		0	00
246	F6		0	00
247	F7		0	00
248	F8		0	00
249	F9		0	00
250	FA		0	00
251	FB		0	00
252	FC		0	00
253	FD		0	00
254	FE		0	00
255	FF		0	00

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(DEC)	/HEV\		SPEC VALUE						
	DEC) (HEX) (REMARKS)								
	INTERNAL USE AREA, 40 BYTES								
19	13	Board Product Name, 30 Byte sequence	80	50					
20	14	"PWR SPLY,1200W,DS1200HE-3-002"	87	57					
21	15	In Decimal = 080, 087, 082, 032, 083, 080, 076, 089, 044, 049, 050, 048, 048, 087, 044, 068, 083,	82	52					
22	16	049, 050, 048, 048, 072, 069, 045, 051, 045, 048, 048, 050, 032	32	20					
23	17	In Hex = 50H, 57H, 52H, 20H, 53H, 50H, 4CH, 59H, 2CH, 31H, 32H, 30H, 30H, 57H, 2CH, 44H,	83	53					
24	18	53H, 31H, 32H, 30H, 30H, 48H, 45H, 2DH, 33H, 2DH, 30H, 30H, 32H, 20H	80	50					
25	19		76	4C					
26	1A		89	59					
27	1B		44	2C					
28	1C		49	31					
29	1D		50	32					
30	1E		48	30					
31	1F		48	30					
32	20		87	57					
33	21		44	2C					
34	22		66	44					
35	23		83	53					
36	24		49	31					
37	25		50	32					
38	26		48	30					
39	27		48	30					
40	28		72	48					
41	29		69	45					
42	2A		45	2D					
43	2B		51	33					
44	2C		45	2D					
45	2D		48	30					
46	2E		48	30					
47	2F		50	32					
48	30		32	20					
75	4B	FRU File ID Data	2	02					
		01h for DS1200HE-3 platform							
		02h for DS1200HE-3-002 platform							
		03h for DS1200HE-3-003 platform							
		04h for DS1200HE-3-004 platform							
79	4F	Board Area Checksum	160	A0					

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OFF	SET	DEFINITION	SPEC '	VALUE						
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)						
	INTERNAL USE AREA, 40 BYTES									
19	13	Board Product Name, 30 Byte sequence	80	50						
20	14	"PWR SPLY,1200W,DS1200HE-3-003"	87	57						
21	15	In Decimal = 080, 087, 082, 032, 083, 080, 076, 089, 044, 049, 050, 048, 048, 087, 044, 068, 083,	82	52						
22	16	049, 050, 048, 048, 072, 069, 045, 051, 045, 048, 048, 051, 032	32	20						
23	17	In Hex = 50H, 57H, 52H, 20H, 53H, 50H, 4CH, 59H, 2CH, 31H, 32H, 30H, 30H, 57H, 2CH, 44H,	83	53						
24	18	53H, 31H, 32H, 30H, 30H, 48H, 45H, 2DH, 33H, 2DH, 30H, 30H, 33H, 20H	80	50						
25	19		76	4C						
26	1A		89	59						
27	1B		44	2C						
28	1C		49	31						
29	1D		50	32						
30	1E		48	30						
31	1F		48	30						
32	20		87	57						
33	21		44	2C						
34	22		66	44						
35	23		83	53						
36	24		49	31						
37	25		50	32						
38	26		48	30						
39	27		48	30						
40	28		72	48						
41	29		69	45						
42	2A		45	2D						
43	2B		51	33						
44	2C		45	2D						
45	2D		48	30						
46	2E		48	30						
47	2F		51	33						
48	30		32	20						
75	4B	FRU File ID Data	3	03						
		01h for DS1200HE-3 platform								
		02h for DS1200HE-3-002 platform								
		03h for DS1200HE-3-003 platform								
		04h for DS1200HE-3-004 platform								
79	4F	Board Area Checksum	158	9E						

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OFF	SET	DEFINITION	SPEC	VALUE						
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)						
	INTERNAL USE AREA, 40 BYTES									
19	13	Board Product Name, 30 Byte sequence	80	50						
20	14	"PWR SPLY,1200W,DS1200HE-3-004"	87	57						
21	15	In Decimal = 080, 087, 082, 032, 083, 080, 076, 089, 044, 049, 050, 048, 048, 087, 044, 068, 083,	82	52						
22	16	049, 050, 048, 048, 072, 069, 045, 051, 045, 048, 048, 052, 032	32	20						
23	17	In Hex = 50H, 57H, 52H, 20H, 53H, 50H, 4CH, 59H, 2CH, 31H, 32H, 30H, 30H, 57H, 2CH, 44H,	83	53						
24	18	53H, 31H, 32H, 30H, 30H, 48H, 45H, 2DH, 33H, 2DH, 30H, 30H, 34H, 20H	80	50						
25	19		76	4C						
26	1A		89	59						
27	1B		44	2C						
28	1C		49	31						
29	1D		50	32						
30	1E		48	30						
31	1F		48	30						
32	20		87	57						
33	21		44	2C						
34	22		66	44						
35	23		83	53						
36	24		49	31						
37	25		50	32						
38	26		48	30						
39	27		48	30						
40	28		72	48						
41	29		69	45						
42	2A		45	2D						
43	2B		51	33						
44	2C		45	2D						
45	2D		48	30						
46	2E		48	30						
47	2F		52	34						
48	30		32	20						
75	4B	FRU File ID Data	4	04						
		01h for DS1200HE-3 platform								
		02h for DS1200HE-3-002 platform								
		03h for DS1200HE-3-003 platform								
		04h for DS1200HE-3-004 platform								
79	4F	Board Area Checksum	156	9C						

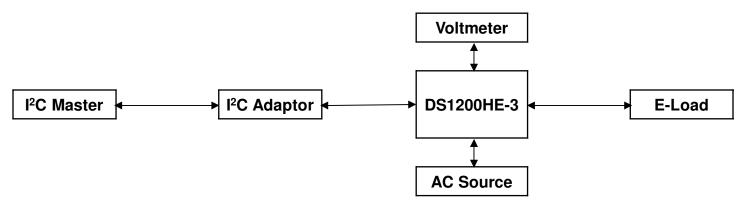
PMBus™ Interface Support

The DS1200HE-3 series is compliant with the industry standard PMBusTM protocol for monitoring and control of the power supply via the I^2C interface port.

DS1200HE-3 Series PMBus™ General Instructions

Equipment Setup

The following is typical I²C communication setup:



PMBus[™] Writing Instructions

When writing to any PMBus™ R/W registers, ALWAYS do the following:

Disable Write Protect (command 10h) by writing any of the following accordingly:

Levels: 00h – Enable writing to all writeable commends

20h - Disables write except 10h, 01h, 00h, 02h and 21h commands

40h - Disables write except 10h, 01h, and 00h commends

80h - Disable write except 0x10h

To save changes on the USER PMBus $^{\text{TM}}$ Table:

Use send byte command: 15h STORE_USER_ALL

To save changes on the DEFAULT PMBus™ Table:

Use send byte command: 11h STORE_DEFAULT_ALL

Wait for 5 seconds, turn-off the PSU, wait for another 5 seconds before turning it on.

DS1200HE-3 Series Support PMBus™ Command List

The DS1200HE-3 is compliant with the industry standard PMBusTM protocol for monitoring and control of the power supply via the i^2C interface port.

DS1200HE-3 Series Supported PMBus $^{\text{TM}}$ Command List:

Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description
01h	OPERATION	80	R/W	1		Used to Turn the unit ON/OFF in conjunction with the input CONTROL pin. It is also used to set output to upper or lower Margin Voltages.
	b7:6	10b				00 – Immediate Turn OFF (No Sequencing) 01 – Soft Turn OFF (With Sequencing) 10 – PSU ON
	b5:4	00b				
	b3:2	00b				
	b1:0	00b				Reserved
02h	ON_OFF_CONFIG	1C	R	1		Configures the combination of CONTROL pin and serial communication commands needed to turn the Unit ON/OFF.
	b7:5	000				Reserved
	b4 – Enable CONTROL pin and Serial communication control.	1				O – Unit powers up any time power is present regardless of the state of CONTROL pin. 1 – Unit powers up as dictated by CONTROL pin and OPERATION command (b3:0)
	b3 – Serial communication Control	1				0 – Unit Ignores ON/OFF portion of the OPERATION command.1 – Enables Serial communication ON/OFF portion of OPERATION command. Requires CONTROL pin to be asserted for the unit to start and energize the output.
	b2 – Sets how the unit responds to CONTROL pin	1				O – Unit ignores CONTROL pin. (ON/OFF controlled by OPERATION command). 1 – Unit requires CONTROL pin to be asserted to start the unit.
	b1 - CONTROL pin polarity	0				0 – Active Low(Pull Low to start the unit) 1 – Active high(Pull high to start the unit)
	b0 – CONTROL pin Action	0				0 – Use programmed turn ON/OFF delay 1 – Turn OFF the output and stop transferring energy to the output as fast as possible.
03h	CLEAR_FAULTS	0	S			
10h	WRITE_PROTECT	00	R/W	1		Used to Control Writing to the PMBus Device 80h - Disables write except 10h 40h - Disables write except 10h, 01h, 00h 20h - Disables write except 10h,01h,00h,02h and 21h commands 00 - Enables write to all writeable commands.
11h	STORE_DEFAULT_ALL	-	S	0		Copies the Value of the Operating memory table to the matching DEFAULT non-volatile memory.
12h	RESTORE_DEFAULT_ALL	-	S	0		Copies the entire contents of the DEFAULT non-volatile memory to the Operating memory table.
15h	STORE_USER_ALL	-	S	0		Copies the Operating memory table to the matching USER non-volatile memory.
16h	RESTORE_USER_ALL	-	S	0		Copies the entire USER non-volatile memory to the Operating memory table.
19h	CAPABILITY	90	R	1		Provides a way for the hosts system to determine some key capabilities of a PMBus device.

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
	b7 - Packet Error Checking	1				0 - PEC not supported 1 - PEC supported
	b6 - Maximum Bus Speed	0				0 - Maximum supported bus speed, 100khz 1 - Maximum supported bus speed, 400khz
	b5 - SMBALERT#	0				0 – SMBus Alert Pin not supported 1 – SMBus Alert Pin supported
	b4:0	00000				Reserved
20h	VOUT_MODE	17	R	1		Specifies the mode and parameters of Output Voltage related Data Formats
21h	VOUT_COMMAND	1819	R/W	2	Linear	Sets the Output Voltage Reference Vout command sends discreet value to change or trim output voltage. The value acts as Digital reference of the Power supply after additional operations are performed (to make the representation compatible). Affects OVP_WARNING and FAULT LIMIT, as well as POWER_GOOD_ON/OFF level.
22h	VOUT_TRIM		R/W	2		Not supported
23h	VOUT_CAL_OFFSET		R/W	2		Not supported
24h	VOUT_MAX	1933	R	2	Linear	Read only. (12.6V)
30h	COEFFICIENTS	NA	BR	6		use to retrieve the m, b and R coefficients, needed for DIRECT data format
	byte 1:2					mlow Byte, m high byte
	byte 3:4					b low Byte, b high byte
	byte 5					R byte
31h	POUT_MAX	NA	R	2	Linear	Sets the operating power limit condition. 1550W
35h	VIN_ON	NA	R	2	Linear	Sets the value of input, in volts, at which the unit should start. ACGOOD 88Vac
36h	VIN_OFF	NA	R	2	Linear	Sets the value of input, in volts, at which the unit should stop power conversion. ACBAD 79Vac
38h	IOUT_CAL_GAIN	NA	R	2		The ratio of voltage across the Current Sense to actual current.
39h	IOUT_CAL_OFFSET	NA	R	2		Used to null any offsets in the current sensing circuit. Normally used in conjunction with the IOUT_SCALE to minimize current sensing error.
3Ah	FAN_ CONFIG_1_2	90	R	1		Read Only to reflect settings of Fans
	b7	1				1 – Fan is installed in position 1 0 – No Fan is installed in position 1
	b6	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC
	b5:4	01				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
	b3	0				1 – Fan is installed in position 2 0 – No fan is installed in position 2
	b2	0				1 - Fan is commanded in RPM 0 - Fan is commanded in DC

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description
	b1:0	0				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
3Bh	FAN_COMMAND_1 *(used by both Fan 1 and 2)	0000	R/W	2	Linear	Adjusts the operation of the Fans. The device may override the command, if it requires higher value, to maintain proper device temperature. RPM Control – Commands Speeds from 0-65535 RPM. Duty cycle Control – Commands Speeds from 0 to 100%
40h	VOUT_OV_FAULT_LIMIT	1A66	R/W	2	Linear	Sets Output Over voltage threshold. (13.2V)
41h	VOUT_OV_FAULT_RESPONSE	80	R	1		Unit Latches OFF. Resets on PSON or CONTROL pin recycle or AC recycle.
42h	VOUT_OV_WARN_LIMIT	1999	R/W	2	Linear	Sets Over-voltage Warning threshold. (12.8V)
43h	VOUT_UV_WARN_LIMIT	1666	R/W	2	Linear	Sets Under-voltage Warning threshold. (11.2V)
44h	VOUT_UV_FAULT_LIMIT	1599	R/W	2	Linear	Sets Under-voltage Fault threshold. (10.8V)
45h	VOUT_UV_FAULT_RESPONSE	80	R	1		Turn PSU OFF
46h	IOUT_OC_FAULT_LIMIT	EB60	R	2	Linear	Sets the Over current threshold in Amps. (108A for Hi Line and Low Line)
47h	IOUT_OC_FAULT_RESPONSE	C0	R	1		OCP ride through. If OCP persists.
4Ah	IOUT_OC_WARN_LIMIT	EB20	R	2	Linear	Sets the Over Current Warning threshold in Amps. (100A for Hi Line and Low Line)
4Fh	OT_FAULT_LIMIT	EB48	R	2	Linear	Secondary ambient temperature Fault threshold, in degree C. (105degC)
50h	OT_FAULT_RESPONSE	F8	R	1		Turn PSU OFF and will retry indefinitely. Support enable/disable of protection and recoverability.
51h	OT_WARN_LIMIT	EB20	R	2	Linear	Secondary ambient temperature warning threshold, in degree C. Operating limit. (100 degC)
5Eh	POWER_GOOD_ON	1766	R	2	Linear	Sets the threshold by which the Power Good signal is asserted. (11.7V)
5Fh	POWER_GOOD_OFF	16CC	R	2	Linear	Sets the threshold by which the Power Good signal is de-asserted. (11.4V)
60h	TON_DELAY	TBD	R	2	Linear	Sets the time (sec), from start condition (Power ON) until the output starts to rise. (2sec)
61h	TON_RISE	8BD7	R	2	Linear	Sets the time (ms), for the output rises from 0 to regulation. (30ms)
64h	TOFF_DELAY	C280	R	2	Linear	Sets the time (ms), from a stop condition (Power OFF) until the output starts to drop (converter OFF). (2.5ms)
78h	STATUS_BYTE	-	R	1		Returns the summary of critical faults
	b7 – BUSY	-				Not supported
	b6 – OFF	-				Unit is OFF
	b5 – VOUT_OV	-				Output over-voltage fault has occurred
	b4 - IOUT_OC	<u> </u>				Output over-current fault has occurred
	b3 - VIN_UV	-				An input undervoltage fault has occurred
	b2 - TEMPERATURE	-				A temperature fault or warning has occurred
	b1 – CML	-				A communication, memory or logic fault has occurred.
	b0 – NONE OF THE ABOVE	-				A Fault Warning not listed in bits[7:1] has occurred.

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description	
79h	STATUS_WORD	-	R	2		Summary of units Fault and warning status.	
	b15 – VOUT					An output voltage fault or warning has occurred	
	b14 – IOUT/POUT					An Output current or power fault or warning has occurred.	
	b13 – INPUT					An input voltage, current or power fault or warning as occurred.	
	b12 – MFR					A manufacturer specific fault or warning has occurred.	
	b11 – POWER_GOOD#					The POWER_GOOD signal is de-asserted	
	b10 - FANS					A fan or airflow fault or warning has occurred.	
	b9 – OTHER					Not supported.	
	b8 – UKNOWN					Not supported.	
	b7 – BUSY					A fault was declared because the device was busy and unable to respond.	
	b6 – OFF					Unit is OFF	
	b5 – VOUT_OV					Output over-voltage fault has occurred	
	b4 – IOUT_OC					Output over-current fault has occurred	
	b3 - VIN_UV					An input under-voltage fault has occurred	
	b2 – TEMPERATURE					A temperature fault or warning has occurred	
	b1 – CML					A communication, memory or logic fault has occurred.	
	b0 - NONE_OF_THE_ABOVE					A fault or warning not listed in bits[7:1] of this byte has occurred.	
7Ah	STATUS_VOUT	-	R	1		Output voltage related faults and warnings	
	b7					VOUT Overvoltage Fault	
	b6					VOUT Over-voltage warning	
	b5					VOUT Under-voltage Warning	
	b4					VOUT Under-voltage Fault	
	b3					VOUT_MAX Warning. Not supported.	
	b2					TON_MAX_FAULT	
	b1					TOFF_MAX Warning . Not supported.	
	b0					Not supported.	
7Bh	STATUS_IOUT		R	1		Output Current related faults and warnings	
	b7					IOUT Over current Fault	
	b6					IOUT Over current And Low Voltage shutdown Fault. Not supported.	
	b5					IOUT Over current Warning	
	b4					IOUT Undercurrent Fault. Not supported.	
	b3					Current Share Fault. Not supported.	
	b2					Power Limiting. Not supported.	
	b1					POUT Overpower Fault. Not supported.	
	b0					POUT Overpower Warning.	
7Ch	STATUS_INPUT	_	R	1		Input related faults and warnings	
. 5.11	b7		- ' '	'		VIN Overvoltage Fault	
						•	
	b6					VIN Overvoltge Warning . Not supported.	

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description	
	b5					VIN Undervoltage Warning	
	b4					VIN Undervoltage Fault	
	b3					Unit is OFF for insufficient Input Voltage	
	b2					IIN Overcurrent Fault	
	b1					IIN Overcurrent Warning . Not supported.	
	b0					PIN overpower Warning . Not supported.	
7Dh	STATUS_TEMPERATURE	-	R	1		Temperature related faults and warnings	
	b7					Overtemperature Fault	
	b6					Overtemperature Warning	
	b5					Undertemperature Warning. Not supported.	
	b4					Undertemperature Fault. Not supported.	
	b3:0					reserved	
7Eh	STATUS_CML	_	R	1		Communications, Logic and Memory	
7 🗀 11	b7		11	'		Invalid or unsupported Command Received	
	b6					Invalid Data	
	b5					Packet Error Check Failed	
	b4					Memory Fault Detect, CRC Error	
						-	
	b3					Not Supported.	
	b2					Not Supported.	
	b1					Not Supported.	
	b0					Not Supported.	
80h	STATUS_MFR_SPECIFIC	-	R	1		Manufacturer Status codes	
	b7					Not Used	
	b6					Not Used	
	b5					Not Used	
	b4					Not Used	
	b3					Not Used	
	b2					Not Used	
	b1					Not Used	
	b0					MFR SPECIFIC FAULT. For Trouble shooting.	
81h	STATUS_FANS_1_2		R	1			
	b7					Fan 1 Fault	
	b6					Fan 2 Fault	
	b5					Fan 1 Warning	
	b4					Fan 2 Warning	
	b3					Fan_1 Speed Overridden	
	b2					Fan_2 Speed Overridden	
	b1					Not Used	
	b0					Not Used	
86h	READ_EIN		R	2	Linear	Returns the accumulated input power over time	
87h	READ_EOUT		R	2	Linear	Returns the accumulated output power over time	
88h	READ_VIN	-	R	2	Linear	Returns input Voltage in Volts ac.	
89h	READ_IIN	-	R	2	Linear	Returns input Current in Amperes	
8Ah	READ_VCAP	-	R	2	Linear	Returns Bulk Capacitor voltage in Volts	
8Bh	READ_VOUT	_	R	2	Linear	Returns the actual, measured voltage in Volts.	

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description	
8Ch	READ_IOUT	-	R	2	Linear	Returns the output current in amperes.	
8Dh	READ_TEMPERATURE_1	-	R	2	Linear	PFC HTSK_Temp_1 (Standard air mode uses this sensor for Fan control)	
8Eh	READ_TEMPERATURE_2	-	R	2	Linear	DCDC HTSK_Temp_2	
8Fh	READ_TEMPERATURE_3	-	R	2	Linear	SEC Ambient_Temp_3 (Inside temperature. Reverse air mode uses this sensor for fan control)	
90h	READ_FAN_SPEED_1	-	R	2	Linear	Speed of Fan 1	
96h	READ_POUT	-	R	2	Linear	Returns the output power, in Watts.	
97h	READ_PIN	-	R	2	Linear	Returns the input power, in Watts.	
98h	PMBUS_REVISION	11	R	1		Reads the PMBus revision number	
	b7:5	0001				Part 1 Revision 0000 – Revision 1.0 0001 – Revision 1.1	
	b4:0	0001				Part 2 Revision 0000 – Revision 1.0 0001 – Revision 1.1	
99h	MFR_ID	07, 45, 4D, 45, 52, 53, 4F, 4E	BR, ASCII	7		Abbrev or symbol of manufacturers name. ASCII (Emerson)	
9Ah	MFR_MODEL		BR, ASCII	13		Manufacturers Model number, ASCII format	
9Bh	MFR_REVISION	TBD	BR, ASCII	3		Manufacturers, revision number, ASCII format. Not Supported.	
9Ch	MFR_LOCATION	0B, 50, 68, 69, 6C, 69, 70, 70, 69, 6E, 65, 73, E0	BR, ASCII	12		Manufacturers facility, ASCII format. (Philippines)	
9Dh	MFR_DATE	TBD	BR	7		Manufacture Date, ASCII format Structure. Not Supported.	
9Eh	MFR_SERIAL	TBD	BR	16		Unit serial number, ASCII format. Not Supported.	
A0h	MFR_VIN_MIN	EADO	R	2	Linear	Minimum Input Voltage (90Vac)	
A1h	MFR_VIN_MAX	FA10	R	2	Linear	Maximum Input Voltage (264Vac)	
A2h	MFR_IIN_MAX		R	2	Linear	Maximum Input Current. Depends on model.	
A3h	MFR_PIN_MAX	0AA3	R	2	Linear	Maximum Input Power	
A4h	MFR_VOUT_MIN	16CC	R	2	Linear	Minimum Output Voltage Regulation Window. (11.4V)	
A5h	MFR_VOUT_MAX	1933	R	2	Linear	Maximum Output Voltage. Regulation Window (12.6V)	
A6h	MFR_IOUT_MAX		R	2	Linear	Maximum Output Current. Depends on model.	
A7h	MFR_POUT_MAX		R	2	Linear	Maximum Output Power. Depends on model.	
A8h	MFR_TAMBIENT_MAX	EA30	R	2	Linear	Maximum Operating Ambient Temperature (Secondary Ambient) (50 degC)	
A9h	MFR_TAMBIENT_MIN	0000	R	2	Linear	Minimum Operating Ambient Temperature (Secondary Ambient) (0 degC)	
AAh	MFR_EFFICIENCY_LL		BR/W	14	Linear	Vin, %load, Eff., %Load, Eff., %load, eff.	
AAh	MFR_EFFICIENCY_HL		BR/W	14	Linear	Vin, %load, Eff., %Load, Eff., %load, eff.	

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	Description	
D0h	Fault Register		R	2		Summary of units Fault and warning status.	
	b15 - 12Vout_sckt					An output short circuit fault has occurred.	
	b14 - 12Vout_ocw					+12V Over Current Warning Flag	
	b13 - 12Vout_ocp2					+12V Fast OCP (High Level OCP) fault occurred (1ms)	
	b12 - 12Vout_ocp					+12V Normal OCP fault occurred (1sec).	
	b11 - 12Vout_ovp2					+12V Second level OVP fault occurred.	
	b10 - 12Vout_ovp					+12V OVP fault occurred.	
	b9 – 12Vout_uvp					+12V UVP fault occurred.	
	b8 – NA					Not Used	
	b7 – NA					Not Used	
	b6 - Ocp_ride_through_flag					PSU is in 1second ride-through because +12V OCP level is reached.	
	b5 – stby_uvp					Standby UVP fault occurred.	
	b4 – fanfail					A fan or airflow fault or warning has occurred.	
	b3 - otp_secondary					Secondary OTP (Ambient) fault occurred.	
	b2 - otp_primary					Primary OTP fault occurred.	
	b1 - PwrLimit_Enabled.					PSU is on Derated Output Power	
	b0 - Save Last Known State IFF "1" - default "0"					Saves Last Known Fault that Occurred. Under Development	
D2h	Min Fan Speed	3923	R	2	L	Standby Fan Speed, (13200 rpm / 20% Duty Cycle)	
D3h	Max Fan Speed	5832	R	2	L	Normal operation Fan Speed (38400 rpm / 100% Duty Cycle)	
E2h	Ishare Offset		R/W	2		Variable. Used by Factory to trim Ishare Voltage Offset. Default before tirmming, 0000	
E3h	Ishare Slope		R/W	2		Variable. Used by Factory to trim Ishare Voltage Slope. Default before tirmming, FF7F	
EAh	ENTER_BOOTLOAD		W	2			
EFh	I/O_EXPANDER		R	1			
F0h	PSU_FACTORY_MODE		R	2			
F8h	FW_VERSION_SEC		BR	2	ASCII		

Application Notes

Current Sharing

The DS1200HE-3 series' main output is equipped with current sharing capability. This will allow up to 4* power supplies to be connected in parallel for higher power application.

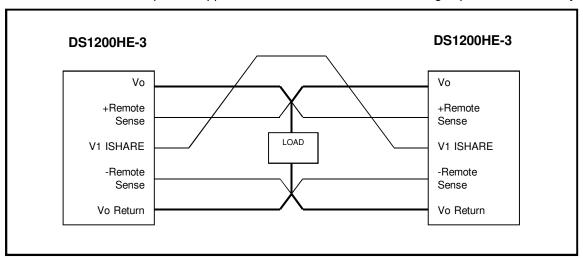
When two or more power supplies are connected and operating in parallel and each is delivering 40-50% of its rated output to the load, the power supplies will current share within 5% accuracy. When supplying light loads between 10% and 30% of its rated load, the power supplies will share within 20% accuracy. (Below 10% load, there is no guarantee of output current sharing) If any power supply is hot swapped, no glitch will occur that violates the regulation limits of the power supply defined in this specification.

During 1+1 operation, the stand-by output load will not exceed the maximum load of a single power supply. Any shutdown, load change, extraction, insertion on a PSU at any load condition should not cause the standby output to fall out of the regulation range.

At minimum, a test in 4+1 configuration will be made by using the DSR1U (73-762-003) rack. The PSU must meet all parametric requirements in any load, line, and environmental conditions mentioned in this specification.

Redundancy / Fault Tolerance

The DS1200HE-3 series power supplies can be connected in the following to provide redundancy/fault tolerance operation:



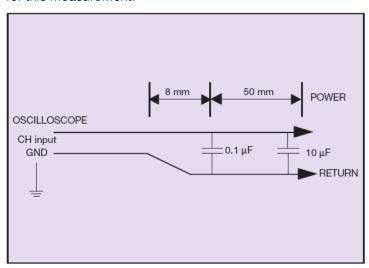
The DS1200HE-3 series power supply will allow up to 4 power supplies to be connected in an N+1 redundant load.

Any failure of one power supply in parallel as well as hot swapping shall not cause more than a 5% change in any output. Current share accuracy is typically 5% of full load. The Failure of one or more supplies will not cause the remaining supplies to violate any of the input or output specifications noted in this specification including all status signals.

The latch of the DS1200HE-3 power supply is designed to prevent the latch from depressed if the AC cord is attached to the power supply. In order to remove the power supply from system chassis, the AC cord must be removed first so the power supply will always be in the powered off state during the removal from system chassis.

Output Ripple and Noise Measurement

The setup outlined in the diagram below has been used for output voltage ripple and noise measurements on the DS1200HE-3 Series. When measuring output ripple and noise, a scope jack in parallel with a 0.1uF ceramic chip capacitor, and a 10 uF aluminum electrolytic capacitor should be used. Oscilloscope should be set to 20 MHz bandwidth for this measurement.



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Record of Revision and Changes

Issue	Date	Description	Originators
1.0	04.30.2014	First Issue	K. Wang
1.1	05.05.2014	Update the error	K. Wang
1.2	08.26.2016	Update the FRU address	K. Wang
1.3	11.17.2016	Update the comment list	K. Wang

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